

# SINGLE BILATRAL ANALOG SWITCH

## 1. Description

The **FLH 74LVC1G66** is a high-speed CMOS device. The device has two data input/output pins (A and B) and an active HIGH enable input pin(C). The device can handle both analog and digital signals.

The signals can be transmitted in either direction when enable pin is high. The analog switch is off when enable pin is low.

### 2. Features

- Operation Voltage Range: 1.65~5.5V
- Inputs Accept Voltages to 5.5V
- Max T<sub>PD</sub> of 0.8 ns at 3.3V
- High Degree of Linearity

## 3. Applications

- Wireless Devices
- Audio and Video Signal Routing
- Portable Computing
- Wearable Devices
- Signal Gating, Chopping, Modulation or Demodulation (Modem)
- Signal Multiplexing for Analog-to-Digital and Digital-to-Analog Conversion Systems.

# 4. Pin Configuration

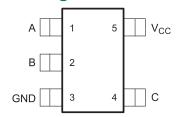


Fig. 1. Pinning Assignments

P	IN	I/O	DESCRIPTION
NAME	NO.	1/0	DESCRIPTION
Α	1	I/O	Bidirectional signal to be
_ ^	ı	1/0	switched
В	2	I/O	Bidirectional signal to be
Б	2	1/0	switched
С	4		Controls the switch (L = OFF, H
	4	I	= ON)
GND	3	_	Ground pin
NC	_	_	Do not connect
Vcc	5	_	Power pin

#### 5. LOGIC DIAGRAM

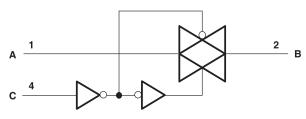


Fig. 2. Logic Diagram (Positive Logic)

# 6. Ordering Information

Type Number	Package Type	Packing
74LVC1G66DBV	SOT-23-5L	Tape & Reel
74LVC1G66DRL	SOT-353	Tape & Reel
74LVC1G66DCK	SC-70	Tape & Reel

Note: If the physical information is inconsistent with the ordering information, please refer to the actual product.



# 7. ABSOLUTE MAXIMUM RATING

(T<sub>A</sub> =25°C, unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage (Note2)	Vcc	-0.5 ~ 6.5	V
Input Voltage	V <sub>IN</sub>	-0.5 ~ 6.5	V
Switch I/O voltage range	V <sub>I/O</sub>	-0.5 ~ V <sub>CC</sub> +0.5	V
Control Input Clamp Current (V <sub>IN</sub> <0)	I <sub>IK</sub>	-50	mA
I/O Port Diode Current (V <sub>I/O</sub> <0 or V <sub>I/O</sub> >Vcc)	I <sub>IOK</sub>	±50	mA
On-state Switch Current (V <sub>I/O</sub> : 0 to V <sub>CC</sub> )	I <sub>T</sub>	±50	mA
V <sub>CC</sub> or GND Current	Icc	±100	mA
Storage Temperature	T <sub>STG</sub>	-65 ~ +150	°C

#### Notes:

- 1. The input and output voltage ratings may be exceeded if the input and output current ratings are observed.
- 2. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

### 8. RECOMMENDED OPERATING COMDITIONS

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Supply Voltage	V <sub>CC</sub>		1.65		5.5	V
Input Voltage	V <sub>IN</sub>		0		5.5	V
I/O Port Voltage	V <sub>I/O</sub>		0		∨CC	V
Operating Temperature	T <sub>A</sub>		-40		85	°C

### 9. STATIC CHARACTERISTICS

PARAMETER	SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNIT	
		V <sub>CC</sub> =1.65V~1.95	0.65×V <sub>CC</sub>			V		
High level Input Voltage	VILI	V <sub>CC</sub> =2.3V~2.7V		1.7			V	
High-level Input Voltage	VIH	V <sub>CC</sub> =3V ~3.6V		2			V	
		V <sub>CC</sub> =4.5V~5.5V	0.7×V <sub>CC</sub>			V		
		V <sub>CC</sub> =1.65V~1.95	ίV			0.35×V <sub>CC</sub>	V	
Low lovel Input Voltage	VIII	V <sub>CC</sub> =2.3V~2.7V				0.7	V	
Low-level Input Voltage	VIL	V <sub>CC</sub> =3V~3.6V			0.8	V		
		V <sub>CC</sub> =4.5V~5.5V			0.3×V <sub>CC</sub>	V		
		V <sub>CC</sub> =1.65V~1.95			20			
Input transition rise/fall	∆t/∆v	V <sub>CC</sub> =2.3V~2.7V			20	20		
time	ZVZV	V <sub>CC</sub> =3V~3.6V				10	ns	
		V <sub>CC</sub> =4.5V~5.5V				10		
			V <sub>CC</sub> =1.65V, I <sub>S</sub> =4mA		12	30		
ON-resistance(rail)	R <sub>ON(rail)</sub>	\/ -CND\/	V <sub>CC</sub> =2.3V, I <sub>S</sub> =8mA		9	20	Ω	
		V <sub>I</sub> =GND or V <sub>CC</sub>	V <sub>CC</sub> =3V, I <sub>S</sub> =24mA		7.5	15		
			V <sub>CC</sub> =4.5V, I <sub>S</sub> =32mA		5.5	10		

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ON-resistance(peak)	(mark)		V <sub>CC</sub> =1.65V, I <sub>S</sub> =4mA	74	4.5	120	
		V -CND or V	V <sub>CC</sub> =2.3V, IS=8mA	20	)	30	Ω
	R <sub>ON(peak)</sub>	V <sub>I</sub> =GND or V <sub>CC</sub>	V <sub>CC</sub> =3V, I <sub>S</sub> =24mA	11	1.5	20	12
			V <sub>CC</sub> =4.5V, I <sub>S</sub> =32mA	7.	5	15	
On-state Switch Leakage	1	VI = VCC or GNI	D, VC =VIH,			±0.1	μA
Current	I <sub>S(ON)</sub>	VO = Open, VC0	C=5.5V			10.1	μΑ
Off-state Switch Leakage	1	$V_I = V_{CC}$ and $V_{O}$	GND or V <sub>I</sub> = GND			±0.1	
Current	I <sub>S(off)</sub>	and $V_0 = V_{CC}$ , $V_0$			±0. Ι	μA	
Control input current	I <sub>I(CTL)</sub>	$V_C = V_{CC}$ or GNE			±0.1	μΑ	
Quiescent Supply Current	I <sub>CC</sub>	$V_C = V_{CC}$ or GNE			1	μΑ	
Additional Quiescent	Δl <sub>CC</sub>	V - V 0 6V V - 5 5V				500	μA
Supply Current	ΔICC	VC - VCC -0.0V, V	$V_{\rm C} = V_{\rm CC} - 0.6 \text{V}, V_{\rm CC} = 5.5 \text{V}$			300	μΑ
Cic Control input	C <sub>IC</sub>	V <sub>CC</sub> =5V		2			pF
capacitance	Cic	VCC-3V					рι
Cio(off) Switch	C <sub>OFF</sub>	V <sub>CC</sub> =5V		6			pF
input/output capacitance	OOFF	V GG-3 V	, CC= <b>⊃</b> ∧				ы
Cio(on) Switch	Con	V <sub>CC</sub> =5V		13	2		pF
input/output capacitance	CON	V00-3V		10	,		ρı

# **10. ANALOG SWITCH CHARACTERISTICS**

PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEST CONDITIONS			UNIT
				V <sub>CC</sub> =1.65V	35	
			C <sub>L</sub> =50pF, R <sub>L</sub> =600Ω,	V <sub>CC</sub> =2.3	120	
			F <sub>IN</sub> =sine wave	V <sub>CC</sub> =3V	175	
Frequency response(1)	A or B	B or A		V <sub>CC</sub> =4.5V	195	MHz
(switch ON)	AUID	D OI A		V <sub>CC</sub> =1.65V	>300	IVIIIZ
			$C_L=5pF, R_L=50\Omega,$	V <sub>CC</sub> =2.3V	>300	
			F <sub>IN</sub> =sine wave	V <sub>CC</sub> =3V	>300	
				V <sub>CC</sub> =4.5V	>300	
0			0 -50-5 D -6000	V <sub>CC</sub> =1.65V	35	mV
Crosstalk	0	A or B	$C_L=50pF, R_L=600\Omega,$	V <sub>CC</sub> =2.3V	50	
(sontrol input to signal	С		F <sub>IN</sub> =1MHZ(square	V <sub>CC</sub> =3V	70	
output)			wave)	V <sub>CC</sub> =4.5V	100	
				V <sub>CC</sub> =1.65V	-58	
			$C_L$ =50pF, RL =600 $\Omega$ ,	V <sub>CC</sub> =2.3V	-58	dB
E III I II (0)			FIN =1MHZ(sine wave)	V <sub>CC</sub> =3V	-58	
Feedthrough attenuation(2)	A or B	B or A		V <sub>CC</sub> =4.5V	-58	
(switch OFF)			0 -5-5 PL -500	V <sub>CC</sub> =1.65V	-42	
			$C_L=5pF$ , $RL=50\Omega$ ,	V <sub>CC</sub> =2.3V	-42	
			FIN =1MHz(sine wave)	V <sub>CC</sub> =3V	-42	



				V <sub>CC</sub> =4.5V	-42	
				V <sub>CC</sub> =1.65V	0.1	
			$C_L$ =50pF, $R_L$ =10KΩ,	V <sub>CC</sub> =2.3V	0.025	
			F <sub>IN</sub> =1KHZ(sine wave)	V <sub>CC</sub> =3V	0.015	
Sine-wave distortion	A or B	B or A		V <sub>CC</sub> =4.5V	0.01	%
Silie-wave distortion	AUID	D OI A		V <sub>CC</sub> =1.65V	0.15	70
			$C_L$ =50pF, R =10k $\Omega$ ,	V <sub>CC</sub> =2.3V	0.025	
			F <sub>IN</sub> =10KHz(sine wave)	V <sub>CC</sub> =3V	0.015	
				V <sub>CC</sub> =4.5V	0.01	

#### Notes:

- 1. Adjust  $f_{\text{IN}}$  voltage to obtain 0 dBm at output. Increase  $f_{\text{IN}}$  frequency until dB meter reads -3dB.
- 2. Adjust  $f_{\text{IN}}$  voltage to obtain 0 dBm at input.

## 11. DYNAMIC CHARACTERISTICS

PARAMETER	SYMBOL	FROM (INPUT)	то (оитрит)	TEST CONDITIONS	MIN	мах	UNIT	
				V <sub>CC</sub> =1.8V±0.15V		2		
Dranagation delay time	+ (1)	A or B	D or A	V <sub>CC</sub> =2.5V±0.2V		1.2	no.	
Propagation delay time	t <sub>PD</sub> (1)	AOID	D OI A	V <sub>CC</sub> =3.3V±0.3V		0.8	ns	
				V <sub>CC</sub> =5V±0.5V		0.6		
				V <sub>CC</sub> =1.8V±0.15V	2.5	12		
Tun-ON time	4 (0)	$ \begin{tabular}{lllllllllllllllllllllllllllllllllll$	A or B	V <sub>CC</sub> =2.5V±0.2V	1.9	6.5	ns	
Tun-ON time	t <sub>EN</sub> (2)			V <sub>CC</sub> =3.3V±0.3V	1.8	5		
			4.2					
				V <sub>CC</sub> =1.8V±0.15V	2.2	10		
Tun-OFF time	+ (2)	C	A or B	V <sub>CC</sub> =2.5V±0.2V	1.4	6.9	ns	
	t <sub>DIS</sub> (3)	C		V <sub>CC</sub> =3.3V±0.3V	2	6.5		
				V <sub>CC</sub> =5V±0.5V	1.4	5		

#### Notes:

 $t_{\text{PLH}}$  and  $t_{\text{PHL}}$  are the same as  $t_{\text{PD}}.$ 

 $t_{\text{PZL}}$  and  $t_{\text{PZH}}$  are the same as  $t_{\text{EN}}.$ 

 $t_{\text{PLZ}}$  and  $t_{\text{PHZ}}$  are the same as  $t_{\text{DIS}}.$ 

# 12. Operating Characteristics

(T<sub>A</sub> =25°C, unless otherwise specified)

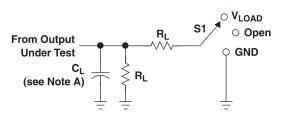
PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Power Dissipation Capacitance	C <sub>PD</sub>	V <sub>CC</sub> =3.3V, f=10MHz		9		pF

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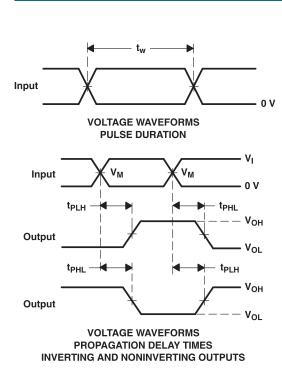
# 13. Load Circuit and Voltage Waveforms

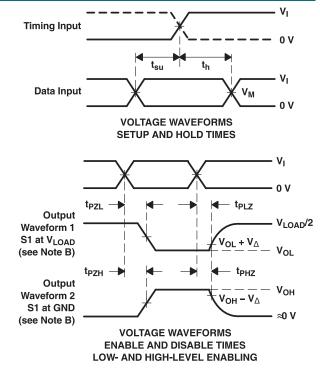


TEST	S1
t <sub>PLH</sub> /t <sub>PHL</sub>	Open
t <sub>PLZ</sub> /t <sub>PZL</sub>	$V_{LOAD}$
t <sub>PHZ</sub> /t <sub>PZH</sub>	GND

**LOAD CIRCUIT** 

Vcc	INPUTS	INPUTS		V <sub>LOAD</sub>	C.	D.	V
	Vı	t <sub>r</sub> /t <sub>f</sub>	V <sub>M</sub>	<b>V</b> LOAD	C∟	R∟	$oldsymbol{V}_\Delta$
1.8 V ± 0.15 V	V <sub>CC</sub>	≼2ns	V <sub>CC</sub> /2	2×V <sub>CC</sub>	30pF	1kΩ	0.15V
2.5 V ± 0.2 V	V <sub>CC</sub>	≼2ns	V <sub>CC</sub> /2	2×V <sub>CC</sub>	30pF	500Ω	0.15V
3.3 V ± 0.3 V	V <sub>CC</sub>	≼2.5ns	V <sub>CC</sub> /2	2×V <sub>CC</sub>	50pF	500Ω	0.3V
5 V ± 0.5 V	Vcc	≼2.5ns	V <sub>CC</sub> /2	2×V <sub>CC</sub>	50pF	500Ω	0.3V





## NOTES:

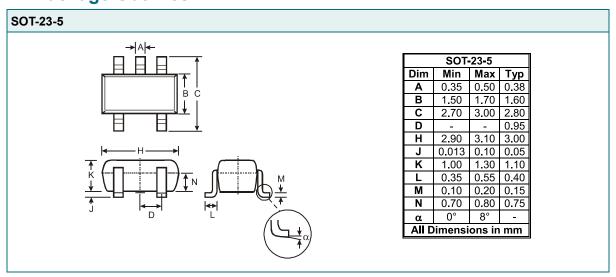
- C<sub>L</sub> includes probe and jig capacitance.
- Waveform 1 is for an output with internal conditions such that the output is low, except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high, except when disabled by the output control.
- All input pulses are supplied by generators having the following characteristics: PRR £ 10 MHz,  $Z_0 = 50$  W.
- The outputs are measured one at a time, with one transition per measurement.
- t<sub>PLZ</sub> and t<sub>PHZ</sub> are the same as t<sub>dis</sub>.
- t<sub>PZL</sub> and t<sub>PZH</sub> are the same as t<sub>en</sub>.
- t<sub>PLH</sub> and t<sub>PHL</sub> are the same as t<sub>pd</sub>.
- All parameters and waveforms are not applicable to all devices.

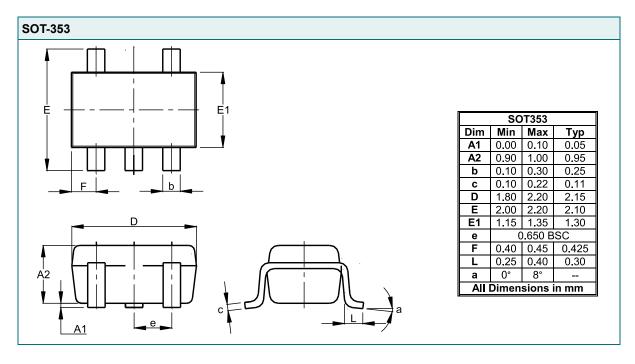
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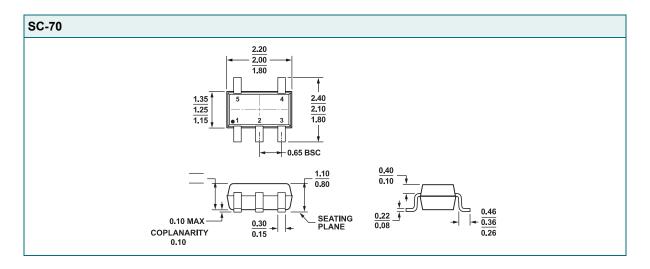


# 14. Package Outlines









### 15. Disclaimers

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