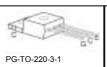


### SGP02N120 SGI02N120

#### Fast IGBT in NPT-technology

- 40% lower *E*<sub>off</sub> compared to previous generation
- Short circuit withstand time 10 μs
- Designed for:
  - Motor controls
  - Inverter
  - SMPS
- NPT-Technology offers:
  - very tight parameter distribution
  - high ruggedness, temperature stable behaviour
  - parallel switching capability







- Qualified according to JEDEC<sup>1</sup> for target applications
- Pb-free lead plating; RoHS compliant
- Complete product spectrum and PSpice Models : http://www.infineon.com/igbt/

Туре	<b>V</b> <sub>CE</sub>	I <sub>C</sub>	$oldsymbol{\mathcal{E}}_{off}$	T <sub>j</sub>	Marking	Package
SGP02N120	1200V	2A	0.11mJ	150°C	GP02N120	PG-TO-220-3-1
SGD02N120	1200V	2A	0.11mJ	150°C	02N120	PG-TO-252-3-11
SGI02N120	1200V	2A	0.11mJ	150°C	GI02N120	PG-TO-262-3-1

#### **Maximum Ratings**

Parameter	Symbol	Value	Unit
Collector-emitter voltage	V <sub>CE</sub>	1200	V
DC collector current	I <sub>C</sub>		А
$T_{\rm C}$ = 25°C		6.2	
$T_{\rm C}$ = 100°C		2.8	
Pulsed collector current, $t_p$ limited by $T_{jmax}$	I <sub>Cpuls</sub>	9.6	
Turn off safe operating area	-	9.6	
$V_{CE} \le 1200 \text{V}, \ T_j \le 150^{\circ} \text{C}$			
Gate-emitter voltage	$V_{GE}$	±20	V
Avalanche energy, single pulse	E <sub>AS</sub>	10	mJ
$I_{\rm C}$ = 2A, $V_{\rm CC}$ = 50V, $R_{\rm GE}$ = 25 $\Omega$ , start at $T_{\rm j}$ = 25 $^{\circ}$ C			
Short circuit withstand time <sup>2</sup>	tsc	10	μs
$V_{\rm GE}$ = 15V, 100V $\leq V_{\rm CC} \leq$ 1200V, $T_{\rm j} \leq$ 150°C			
Power dissipation	P <sub>tot</sub>	62	W
$T_{\rm C}$ = 25°C			
Operating junction and storage temperature	$T_{\rm j}$ , $T_{ m stg}$	-55+150	°C
Soldering temperature, PG-TO252 (reflow soldering, MSL3) Other packages: 1.6mm (0.063 in.) from case for 10s	-	260 260	

<sup>&</sup>lt;sup>1</sup> J-STD-020 and JESD-022

<sup>&</sup>lt;sup>2</sup> Allowed number of short circuits: <1000; time between short circuits: >1s.



# SGD02N120, SGI02N120

# SGP02N120

#### **Thermal Resistance**

Parameter	Symbol	Conditions	Max. Value	Unit
Characteristic	-			
IGBT thermal resistance,	R <sub>thJC</sub>		2.0	K/W
junction – case				
Thermal resistance,	$R_{thJA}$	PG-TO-220-3-1	62	
junction – ambient		PG-TO-262-3-1		
SMD version, device on PCB <sup>1)</sup>	$R_{thJA}$	PG-TO-252-3-11	50	

#### **Electrical Characteristic,** at $T_j$ = 25 °C, unless otherwise specified

Davameter	Cumbal	Conditions		Value		Unit
Parameter	Symbol	Conditions	min.	typ.	max.	
Static Characteristic	1					•
Collector-emitter breakdown voltage	$V_{(BR)CES}$	$V_{\rm GE} = 0  \text{V}, I_{\rm C} = 100  \mu \text{A}$	1200	-	-	V
Collector-emitter saturation voltage	$V_{CE(sat)}$	$V_{\rm GE} = 15 \text{V}, I_{\rm C} = 2 \text{A}$				
		<i>T</i> <sub>j</sub> =25°C	2.5	3.1	3.6	
		T <sub>j</sub> =150°C	-	3.7	4.3	
Gate-emitter threshold voltage	$V_{\rm GE(th)}$	$I_{\rm C} = 100 \mu A, V_{\rm CE} = V_{\rm GE}$	3	4	5	
Zero gate voltage collector current	I <sub>CES</sub>	V <sub>CE</sub> =1200V, V <sub>GE</sub> =0V				μА
		T <sub>j</sub> =25°C	-	-	25	
		T <sub>j</sub> =150°C	-	-	100	
Gate-emitter leakage current	I <sub>GES</sub>	V <sub>CE</sub> =0V, V <sub>GE</sub> =20V	-	-	100	nA
Transconductance	$g_{fs}$	V <sub>CE</sub> =20V, I <sub>C</sub> =2A		1.5	-	S
Dynamic Characteristic			,		,	
Input capacitance	Ciss	V <sub>CE</sub> =25V,	-	205	250	pF
Output capacitance	Coss	$V_{GE}=0V$ ,	-	20	25	
Reverse transfer capacitance	Crss	f=1MHz	-	12	14	
Gate charge	Q <sub>Gate</sub>	V <sub>CC</sub> =960V, I <sub>C</sub> =2A	-	11	-	nC
		V <sub>GE</sub> =15V				
Internal emitter inductance	LE		-	7	-	nΗ
measured 5mm (0.197 in.) from case						
Short circuit collector current <sup>2)</sup>	$I_{C(SC)}$	$V_{\text{GE}} = 15V, t_{\text{SC}} \le 10 \mu \text{s}$ $100V \le V_{\text{CC}} \le 1200V,$ $T_{\text{j}} \le 150^{\circ}\text{C}$	-	24	-	A

2 **Power Semiconductors** Rev. 2.3 Sep. 07

 $<sup>^{1)}</sup>$  Device on 50mm\*50mm\*1.5mm epoxy PCB FR4 with 6cm² (one layer, 70µm thick) copper area for collector connection. PCB is vertical without blown air.  $^{2)}$  Allowed number of short circuits: <1000; time between short circuits: >1s.



# SGD02N120, SGI02N120

### SGP02N120 SGI02N120

#### Switching Characteristic, Inductive Load, at $T_j$ =25 °C

Parameter	Symbol	Conditions	Value			Unit
raianietei	Symbol	Conditions	min.	typ.	max.	Oilit
IGBT Characteristic						
Turn-on delay time	$t_{d(on)}$	T <sub>j</sub> =25°C,	-	23	30	ns
Rise time	t <sub>r</sub>	$V_{CC} = 800 \text{V}, I_{C} = 2\text{A},$	-	16	21	
Turn-off delay time	$t_{d(off)}$	$V_{\rm GE}$ =15V/0V,	-	260	340	
Fall time	$t_{f}$	$R_{\rm G} = 91\Omega$ ,	-	61	80	
Turn-on energy	Eon	$L_{\sigma}^{(1)} = 180 \text{nH},$ $C_{\sigma}^{(1)} = 40 \text{pF}$	-	0.16	0.21	mJ
Turn-off energy	$E_{off}$	Energy losses include	-	0.06	0.08	
Total switching energy	E <sub>ts</sub>	"tail" and diode reverse recovery.	-	0.22	0.29	

#### Switching Characteristic, Inductive Load, at $T_j$ =150 °C

Parameter	Cumbal	Conditions	Value			Hait
Parameter	Symbol	Conditions	min.	typ.	max.	ns mJ
IGBT Characteristic						
Turn-on delay time	$t_{d(on)}$	T <sub>j</sub> =150°C	-	26	31	ns
Rise time	$t_{r}$	V <sub>CC</sub> =800V,	-	14	17	
Turn-off delay time	$t_{d(off)}$	I <sub>C</sub> =2A,	-	290	350	
Fall time	$t_{\mathrm{f}}$	$V_{\text{GE}}$ =15V/0V,	-	85	102	
Turn-on energy	Eon	$R_{\rm G} = 91\Omega$ ,	1	0.27	0.33	mJ
Turn-off energy	E <sub>off</sub>	$L_{\sigma}^{1)}$ =180nH, $C_{\sigma}^{1)}$ =40pF	-	0.11	0.15	
Total switching energy	E <sub>ts</sub>	Energy losses include "tail" and diode reverse recovery.	-	0.38	0.48	

 $<sup>^{1)}</sup>$  Leakage inductance  $L_{\sigma}$  and stray capacity  $C_{\sigma}$  due to dynamic test circuit in figure E.



### SGP02N120 SGI02N120

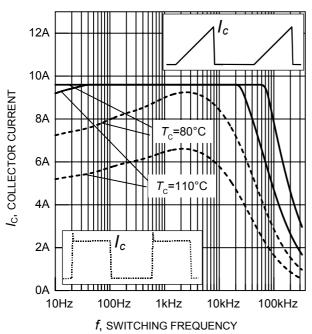


Figure 1. Collector current as a function of switching frequency

 $(T_{\rm j} \le 150^{\circ}\text{C}, D = 0.5, V_{\rm CE} = 800\text{V}, V_{\rm GE} = +15\text{V}/0\text{V}, R_{\rm G} = 91\Omega)$ 

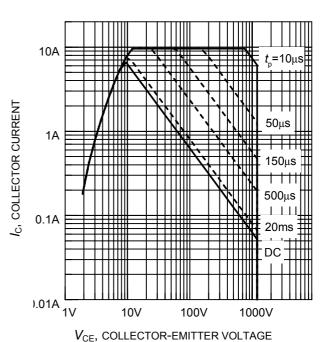


Figure 2. Safe operating area  $(D = 0, T_C = 25^{\circ}C, T_i \le 150^{\circ}C)$ 

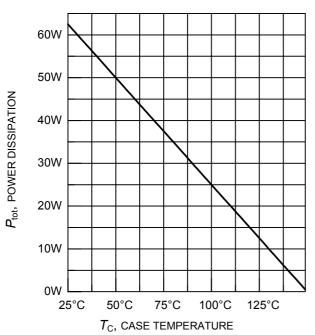


Figure 3. Power dissipation as a function of case temperature

 $(T_i \le 150^{\circ}C)$ 

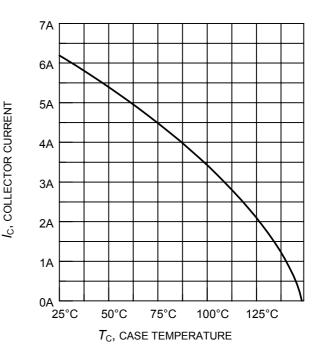


Figure 4. Collector current as a function of case temperature

 $(V_{GE} \le 15V, T_i \le 150^{\circ}C)$ 



C, COLLECTOR CURRENT

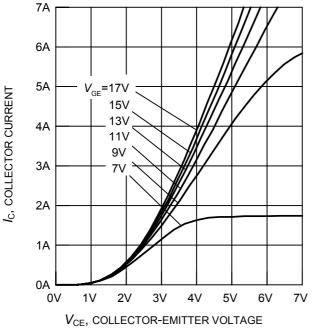


Figure 5. Typical output characteristics  $(T_j = 25^{\circ}C)$ 

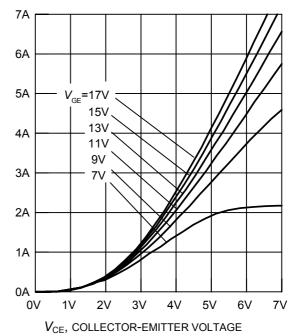


Figure 6. Typical output characteristics  $(T_i = 150^{\circ}\text{C})$ 

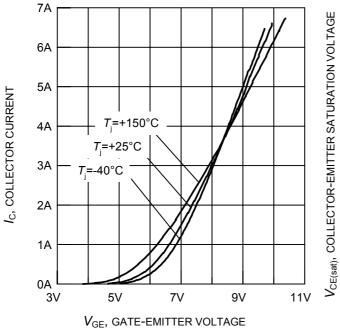


Figure 7. Typical transfer characteristics ( $V_{CE} = 20V$ )

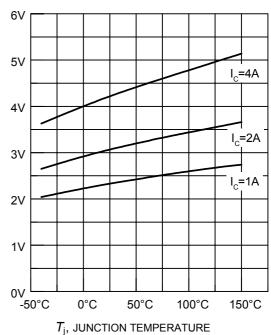


Figure 8. Typical collector-emitter saturation voltage as a function of junction temperature ( $V_{GE} = 15V$ )



t, SWITCHING TIMES

 $V_{\text{GE(th)}}$ , GATE-EMITTER THRESHOLD VOLTAGE

### SGP02N120 SGI02N120

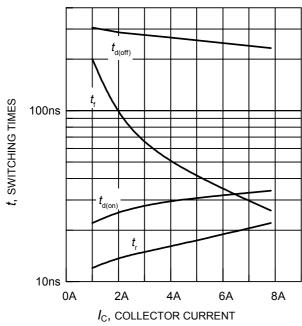


Figure 9. Typical switching times as a function of collector current (inductive load,  $T_{\rm j}$  = 150°C,  $V_{\rm CE}$  = 800V,  $V_{\rm GE}$  = +15V/0V,  $R_{\rm G}$  = 91 $\Omega$ , dynamic test circuit in Fig.E)

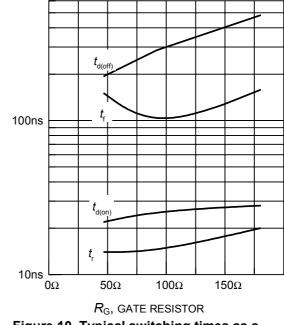


Figure 10. Typical switching times as a function of gate resistor (inductive load,  $T_j = 150^{\circ}\text{C}$ ,  $V_{\text{CE}} = 800\text{V}$ ,  $V_{\text{GE}} = +15\text{V/OV}$ ,  $I_{\text{C}} = 2\text{A}$ , dynamic test circuit in Fig.E)

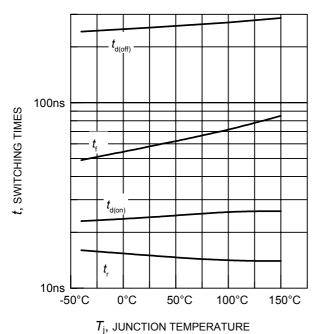


Figure 11. Typical switching times as a function of junction temperature (inductive load,  $V_{\text{CE}} = 800\text{V}$ ,  $V_{\text{GE}} = +15\text{V}/0\text{V}$ ,  $I_{\text{C}} = 2\text{A}$ ,  $R_{\text{G}} = 91\Omega$ , dynamic test circuit in Fig.E)

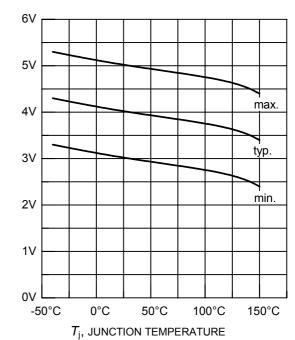


Figure 12. Gate-emitter threshold voltage as a function of junction temperature  $(I_C = 0.3 \text{mA})$ 

Sep. 07



### SGP02N120 SGI02N120

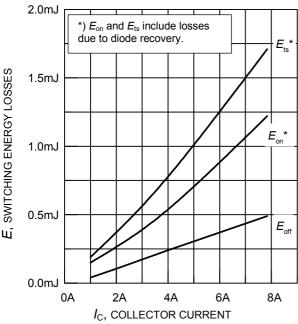


Figure 13. Typical switching energy losses as a function of collector current (inductive load,  $T_{\rm j}$  = 150°C,  $V_{\rm CE}$  = 800V,  $V_{\rm GE}$  = +15V/0V,  $R_{\rm G}$  = 91 $\Omega$ ,

dynamic test circuit in Fig.E)

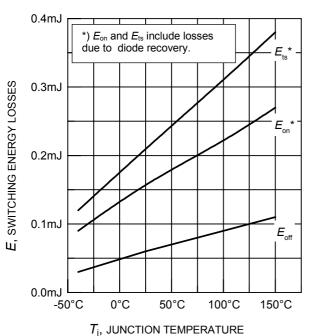


Figure 15. Typical switching energy losses as a function of junction temperature (inductive load,  $V_{\text{CE}} = 800\text{V}$ ,  $V_{\text{GE}} = +15\text{V}/0\text{V}$ ,  $I_{\text{C}} = 2\text{A}$ ,  $R_{\text{G}} = 91\Omega$ , dynamic test circuit in Fig.E )

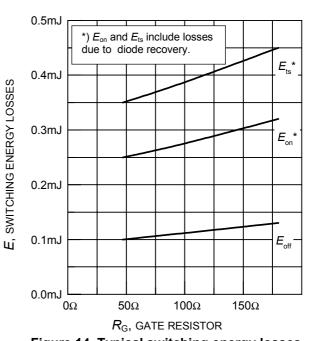


Figure 14. Typical switching energy losses as a function of gate resistor (inductive load,  $T_j = 150^{\circ}\text{C}$ ,  $V_{\text{CE}} = 800\text{V}$ ,  $V_{\text{GE}} = +15\text{V}/0\text{V}$ ,  $I_{\text{C}} = 2\text{A}$ , dynamic test circuit in Fig.E )

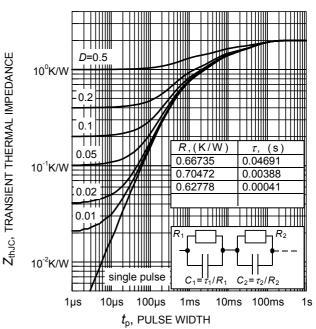
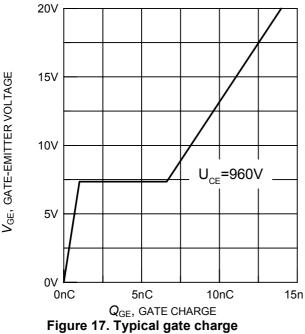


Figure 16. IGBT transient thermal impedance as a function of pulse width  $(D = t_0 / T)$ 



C, CAPACITANCE



 $(I_{\rm C} = 2A)$ 

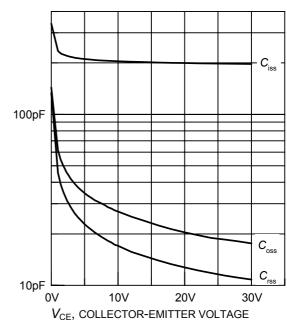


Figure 18. Typical capacitance as a function of collector-emitter voltage  $(V_{GE} = 0V, f = 1MHz)$ 

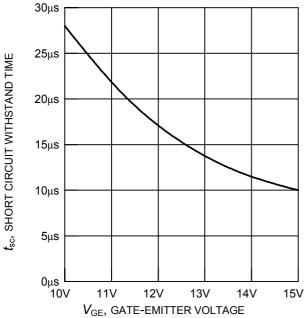


Figure 19. Short circuit withstand time as a function of gate-emitter voltage  $(V_{CE} = 1200 \text{V}, \text{ start at } T_i = 25^{\circ}\text{C})$ 

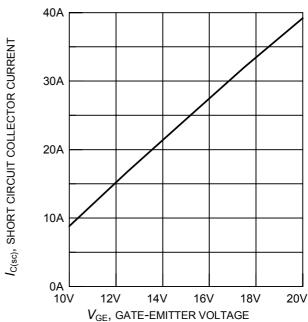
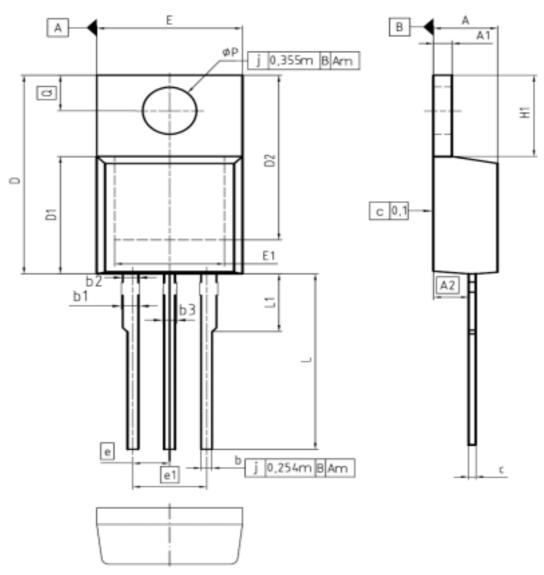


Figure 20. Typical short circuit collector current as a function of gate-emitter voltage  $(100V \le V_{CE} \le 1200V, T_C = 25^{\circ}C, T_i \le 150^{\circ}C)$ 

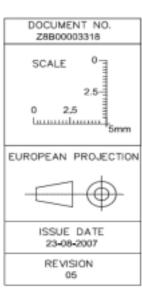


# SGP02N120 SGI02N120

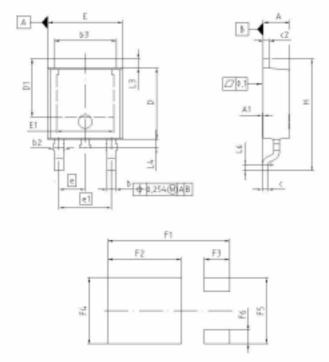
#### PG-TO220-3-1



DIM	MILLIM	ETERS	INCHES		
DIM	MIN	MAX	MIN	MAX	
Α	4.30	4.57	0.169	0.180	
A1	1.17	1.40	0.046	0.055	
A2	2.15	2.72	0,085	0.107	
ь	0.65	0.86	0,026	0.034	
ь1	0.95	1.40	0.037	0.055	
ь2	0.95	1.15	0,037	0.045	
ь3	0,65	1,15	0,026	0,045	
С	0.33	0.60	0.013	0.024	
D	14.81	15.95	0.583	0.628	
D1	8.51	9.45	0,335	0.372	
D2	12.19	13.10	0.480	0.516	
E	9.70	10.36	0.382	0.408	
E1	6,50	8,60	0,256	0,339	
e	2.	54	0.100		
e1	5.	08	0.2	900	
N		3		3	
H1	5.90	6.90	0.232	0.272	
L	13.00	14.00	0.512	0.551	
L1	-	4.80	-	0.189	
вP	3.60	3.89	0.142	0.153	
Q	2.60	3.00	0.102	0.118	







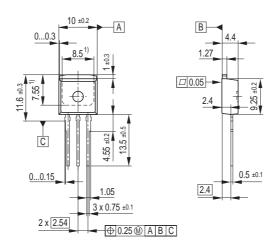
PG-TO252-3-11

DIM	MILLIM	ETERS	INCI	·es
DIM	MIN	MAX	MIN	MAX
A	2.194	2.388	0.068	0.094
A1	0.000	0.150	0.000	0.006
b	0.835	0.889	0.025	0.035
b2	0.650	1.150	0.025	0.045
b3	5,004	5.500	0.197	0.217
0	0.490	0.580	0.048	0.023
62	9,490	0.960	0.048	0.039
D	5.969	6.223	0.235	0.245
D4	5.020	5.320	0.196	0.209
E	5.400	6.734	0.252	0.265
E1	4.900	5.100	0.193	0.201
	2.296		0.0	190
e1	4,5	72	0.1	183-
N	3	;		3
н	9,400	10,094	0.370	0.397
L3	0.900	1,118	0.095	0.044
L4	0.690	1,016	0.026	0.040
LG	0.510	0.686	0.029	0.027
P1	10.500	10.700	0.413	0.421
F2	6.300	5.500	0.248	0.256
F3	2.900	2.300	0.063	0.091
F4	5.700	5.900	0.224	0.232
FS	5,660	5.880	0.222	0.231
F6	1.100	1,300	0.043	0.051



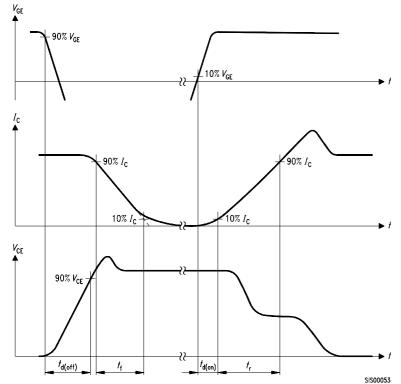
# SGP02N120 SGI02N120

#### PG-TO262-3-1 (I<sup>2</sup> Pak)



Typical Metal surface min. X = 7.25, Y = 6.9 All metal surfaces tin plated, except area of cut.





 $di_{F}/dt \qquad t_{rr} = t_{S} + t_{F}$   $Q_{rr} = Q_{S} + Q_{F}$   $t_{rr} \qquad t_{F} \qquad t_{F}$ 

Figure C. Definition of diodes switching characteristics

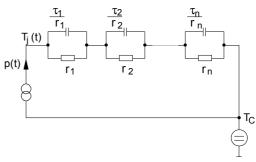


Figure A. Definition of switching times

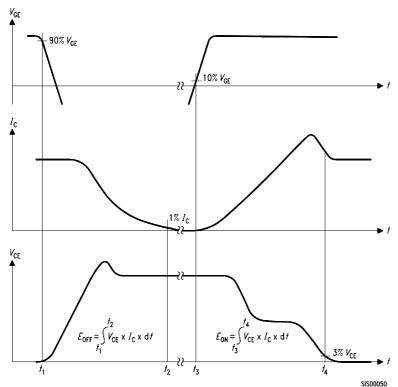


Figure D. Thermal equivalent circuit

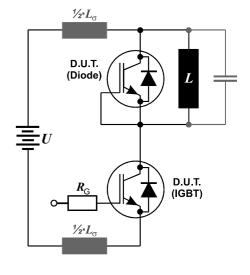


Figure B. Definition of switching losses

Figure E. Dynamic test circuit Leakage inductance  $L_{\sigma}$ =180nH, and stray capacity  $C_{\sigma}$  =40pF.



### SGP02N120 SGI02N120

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