

PNP SILICON TRIPLE DIFFUSED TRANSISTOR
MP-3

DESCRIPTION

2SA1412-Z is designed for High Voltage Switching, especially in Hybrid Integrated Circuits.

FEATURES

- High Voltage : $V_{CE0} = -400$ V
- High Speed : $t_r \leq 0.7$ μ s
- Complement to 2SC3631-Z

QUALITY GRADE

Standard

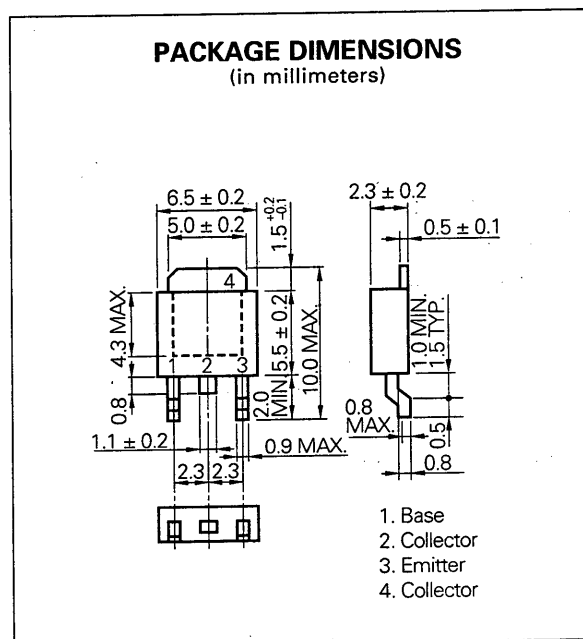
Please refer to "Quality grade on NEC Semiconductor Devices" (Document number IEI-1209) published by NEC Corporation to know the specification of quality grade on the devices and its recommended applications.

ABSOLUTE MAXIMUM RATINGS ($T_a = 25$ °C)

Collector to Base Voltage	V_{CBO}	-400	V
Collector to Emitter Voltage	V_{CEO}	-400	V
Emitter to Base Voltage	V_{EBO}	-7	V
Collector Current (DC)	I_C	-2.0	A
Collector Current (Pulse)*	I_C	-4.0	A
Total Power Dissipation ($T_a = 25$ °C)**	P_T	2.0	W
Junction Temperature	T_j	150	°C
Storage Temperature	T_{stg}	-55 to +150	°C

* $PW \leq 10$ ms, Duty Cycle ≤ 50 %

** When mounted on ceramic substrate of $7.5 \text{ cm}^2 \times 0.7$ mm

PACKAGE DIMENSIONS
(in millimeters)

ELECTRICAL CHARACTERISTICS (T_a = 25 °C)

CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITIONS
Collector Cutoff Current	I _{cBO}			-10	μA	V _{CB} = -400 V, I _E = 0
Emitter Cutoff Current	I _{EBO}			-10	μA	V _{EB} = -5.0 V, I _C = 0
DC Current Gain	h _{FE1} *	40	60	120		V _{CE} = -5.0 V, I _C = -0.1 A
DC Current Gain	h _{FE2} *	10	22			V _{CE} = -5.0 V, I _C = -1.0 A
Collector Saturation Voltage	V _{CE(sat)} *		-0.25	-0.5	V	I _C = -0.5 A, I _B = -0.1 A
Base Saturation Voltage	V _{BE(sat)} *		-0.85	-1.2	V	I _C = -0.5 A, I _B = -0.1 A
Gain Bandwidth Product	f _T		40		MHz	V _{CE} = -10 V, I _E = -100 mA
Output Capacitance	C _{ob}		30		pF	V _{CB} = -10 V, I _E = 0, f = 1.0 MHz
Turn-on Time	t _{on}		0.03	0.5	μs	I _C = -1.0 A, R _L = 150 Ω I _{B1} = -I _{B2} = -0.2 A, V _{CC} = -150 V
Storage Time	t _{stg}		1.4	2.0	μs	
Fall time	t _r		0.1	0.7	μs	

* Pulsed: PW ≤ 350 μs, Duty Cycle ≤ 2 %

h_{FE} Classification

MARKING	L	K
h _{FE1}	40 to 80	60 to 120

TYPICAL CHARACTERISTICS (T_a = 25 °C)

