NPN SILICON POWER TRANSISTOR 2SD882

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DESCRIPTION

NEC

The 2SD882 is NPN silicon transistor suited for the output stage of 3 watts audio amplifier, voltage regulator, DC-DC converter and relay driver.

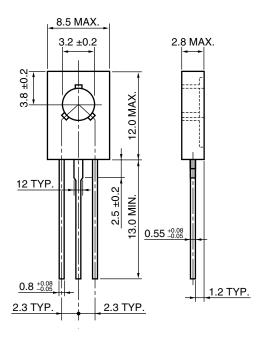
FEATURES

- Low saturation voltage
 - $V_{CE(sat)} = 0.5 V MAX. (I_C = -2 A, I_B = 0.2 A)$
- Excellent hFE linearity and high hFE hFE = 60 to 400 (VCE = 2 V, IC = 1 A)
- Less cramping space required due to small and thin package and reducing the trouble for attachment to a radiator. No insulator bushing required.

ABSOLUTE MAXIMUM RATINGS

Maximum Te	mperature					
Storage Te	–55 to +150°C					
Junction Te	150°C Maximum					
Maximum Power Dissipations						
Total Powe	1.0 W					
Total Powe	10 W					
Maximum Voltages and Currents ($T_A = 25^{\circ}C$)						
Vсво	Collector to Base Voltage	40 V				
VCEO	Collector to Emitter Voltage	30 V				
Vebo	Emitter to Base Voltage	5.0 V				
C(DC)	Collector Current (DC)	3.0 A				
C(pulse)	Collector Current (pulse)	7.0 A				
Note Pulse Test PW \leq 350 μ s, Duty Cycle \leq 2%						

* PACKAGE DRAWING (Unit: mm)



1: Emitter

2: Collector: connected to mounting plane

3: Base

ELECTRICAL CHARACTERISTICS (TA = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
DC Current Gain	h _{FE1}	Vce = 2.0 V, Ic = 20 mA ^{Note}	30	150		
DC Current Gain	hfe2	V _{CE} = 2.0 V, I _C = 1.0 A ^{Note}	60	160	400	
Gain Bandwidth Product	f⊤	Vce = 5.0 V, Ic = 0.1 A		90		MHz
Output Capacitance	Cob	V _{CB} = 10 V, I _E = 0, f = 1.0 MHz		45		pF
Collector Cutoff Current	Ісво	V _{CB} = 30 V, I _E = 0 A			1.0	μA
Emitter Cutoff Current	Іево	V _{EB} = 3.0 V, I _C = 0 A			1.0	μA
Collector Saturation Voltage	VCE(sat)	Ic = 2.0 A, I _B = 0.2 A ^{Note}		0.3	0.5	V
Base Saturation Voltage	V _{BE(sat)}	I_{C} = 2.0 A, I_{B} = 0.2 A ^{Note}		1.0	2.0	V

Note Pulse Test: PW \leq 350 μ s, Duty Cycle \leq 2%

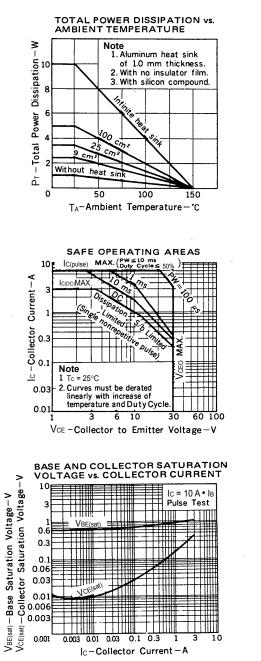
CLASSIFICATION OF hFE

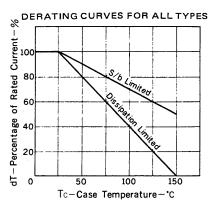
Rank	R	Q	Р	E
Range	60 to 120	100 to 200	160 to 320	200 to 400

Remark Test Conditions: VCE = 2.0 V, IC = 1.0 A

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COLLECTOR CURRENT vs. COLLECTOR TO EMITTER VOLTAGE

5

12

4

10

-9

Pulse Test

2

20

 $|_B = 1 mA$

0.3

16

2.0

4

Current

Collector Collector

0.4

0

4

8

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Σ

Product -

Bandwidth

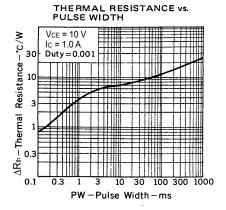
-Gain

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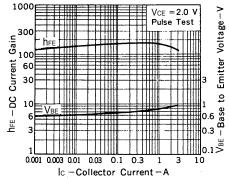
10

0.01

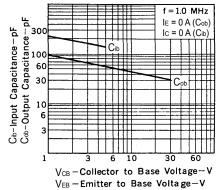
0.03

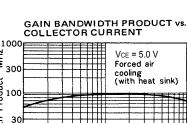






INPUT AND OUTPUT CAPACITANCE vs. REVERSE VOLTAGE





0.1

Ic-Collector Current-A

VCE-Collector to Emitter Voltage-V