

#### Is Now Part of



## ON Semiconductor®

To learn more about ON Semiconductor, please visit our website at <a href="https://www.onsemi.com">www.onsemi.com</a>

ON Semiconductor and the ON Semiconductor logo are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using ON Semiconductor products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by ON Semiconductor. "Typical" parameters which may be provided in ON Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. ON Semiconductor does not convey any license under its patent rights nor the rights of others. ON Semiconductor products are not designed, intended, or authorized for use as a critical component in life support systems or any EDA Class 3 medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use ON Semiconductor products for any such unintended or unauthorized application, Buyer shall indemnify and hold ON Semiconductor and its officers, employees, emplo



### **KSC2330**

#### **Color TV Chroma Output**

- Collector-Base Voltage: V<sub>CBO</sub>=300V
  Current Gain Bandwidth Product: f<sub>T</sub>=50MHz (TYP.)



1. Emitter 2. Collector 3. Base

### **NPN Epitaxial Silicon Transistor**

#### **Absolute Maximum Ratings** $T_a$ =25°C unless otherwise noted

Symbol	Parameter	Ratings	Units
$V_{CBO}$	Collector-Base Voltage	300	V
V <sub>CEO</sub>	Collector-Emitter Voltage	300	V
V <sub>EBO</sub>	Emitter-Base Voltage	7	V
I <sub>C</sub>	Collector Current	100	mA
P <sub>C</sub>	Collector Power Dissipation	1	W
TJ	Junction Temperature	150	°C
T <sub>STG</sub>	Storage Temperature	-55 ~ +150	°C

#### **Electrical Characteristics** $T_a$ =25°C unless otherwise notd

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
BV <sub>CBO</sub>	Collector-Base Breakdown Voltage	$I_C=100\mu A, I_E=0$	300			V
BV <sub>CEO</sub>	Collector-Emitter Breakdown Voltage	I <sub>C</sub> =5mA, I <sub>B</sub> =0	300			V
BV <sub>EBO</sub>	Emitter-Base Breakdown Voltage	$I_E=100\mu A, I_C=0$	7			V
I <sub>CBO</sub>	Collector Cut-off Current	V <sub>CB</sub> =200V, I <sub>E</sub> =0			0.1	μΑ
h <sub>FE</sub>	DC Current Gain	V <sub>CE</sub> =10V, I <sub>C</sub> =20mA	40		240	
V <sub>CE</sub> (sat)	Collector-Emitter Saturation Voltage	I <sub>C</sub> =10mA, I <sub>B</sub> =1mA			0.5	V
f <sub>T</sub>	Current Gain Bandwidth Product	V <sub>CE</sub> =30V, I <sub>C</sub> =10mA		50		MHz
C <sub>ob</sub>	Output Capacitance	V <sub>CB</sub> =10V, I <sub>E</sub> =0, f=1MHz		4		pF

### **h**<sub>FE</sub> Classification

Classification	R	0	Y
h <sub>FE</sub>	40 ~ 80	70 ~ 140	120 ~ 240

# **Typical Characteristics**

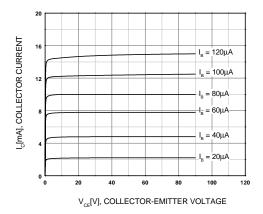


Figure 1. Static Characteristic

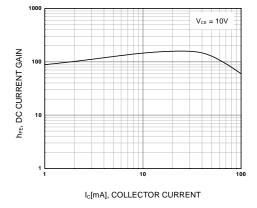


Figure 2. DC current Gain

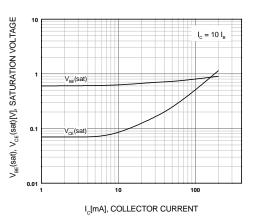


Figure 3. Base-Emitter Saturation Voltage Collector-Emitter Saturation Voltage

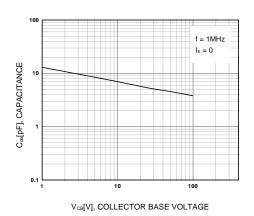


Figure 4. Collector Output Capacitance

Rev. A2, September 2002

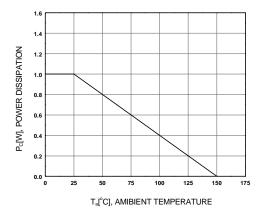


Figure 5. Power Derating



## **Package Dimensions**

### TO-92L

