MJE270G (NPN), MJE271G (PNP)

Complementary Silicon Power Transistors

Features

- High Safe Operating Area
 - $I_{S/B}$ @ 40 V, 1.0 s = 0.375 A
- Collector–Emitter Sustaining Voltage
 - $V_{CEO(sus)} = 100 \text{ Vdc (Min)}$
- High DC Current Gain
 - h_{FE} @ 120 mA, 10 V = 1500 (Min)
- These Devices are Pb-Free and are RoHS Compliant*

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector–Emitter Voltage	V _{CEO}	100	Vdc
Collector-Base Voltage	V _{CB}	100	Vdc
Emitter-Base Voltage	V _{EB}	5.0	Vdc
Collector Current – Continuous	I _C	2.0	Adc
Collector Current – Peak	I _{CM}	4.0	Adc
Base Current	Ι _Β	0.1	Adc
Total Power Dissipation @ T _C = 25°C Derate above 25°C	P _D	15 0.12	W W/°C
Total Power Dissipation @ T _A = 25°C Derate above 25°C	P _D	1.5 0.012	W W/°C
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-65 to +150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

THERMAL CHARACTERISTICS

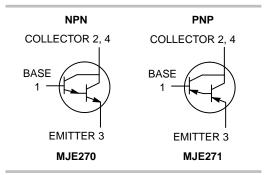
Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	8.33	°C/W
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	83.3	°C/W



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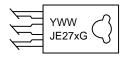
http://onsemi.com

2.0 AMPERE COMPLEMENTARY POWER DARLINGTON TRANSISTORS 100 VOLTS, 15 WATTS





MARKING DIAGRAM



Y = Year

WW = Work Week

JE27x = Specific Device Code

x = 0 or 1

G = Pb-Free Package

ORDERING INFORMATION

Device	Package	Shipping
MJE270G	TO-225 (Pb-Free)	500 Units/Box
MJE271G	TO-225 (Pb-Free)	500 Units/Box

^{*}For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS	•			
Collector–Emitter Sustaining Voltage (Note 1) $(I_C = 10 \text{ mAdc}, I_B = 0)$	V _{CEO(sus)}	100	_	Vdc
Collector Cutoff Current (V _{CE} = 100 Vdc, I _B = 0)	I _{CEO}	_	1.0	mAdc
Collector Cutoff Current (V _{CB} = 100 Vdc, I _E = 0)	Ісво	-	0.3	mAdc
Emitter Cutoff Current (V _{BE} = 5.0 Vdc, I _C = 0)	I _{EBO}	-	0.1	mAdc
SECOND BREAKDOWN				
Second Breakdown Collector Current with Base Forward Biased (V _{CE} = 40 Vdc, t = 1.0 s, Non-repetitive)	I _{S/b}	375	_	Adc
ON CHARACTERISTICS (Note 1)				
DC Current Gain ($I_C = 20 \text{ mAdc}$, $V_{CE} = 3.0 \text{ Vdc}$) ($I_C = 120 \text{ mAdc}$, $V_{CE} = 10 \text{ Vdc}$)	h _{FE}	500 1500	- -	_
Collector–Emitter Saturation Voltage ($I_C = 20 \text{ mAdc}$, $I_B = 0.2 \text{ mAdc}$) ($I_C = 120 \text{ mAdc}$, $I_B = 1.2 \text{ mAdc}$)	V _{CE(sat)}	_ _	2.0 3.0	Vdc
Base–Emitter On Voltage (I _C = 120 mAdc, V _{CE} = 10 Vdc)	V _{BE(on)}	-	2.0	Vdc
DYNAMIC CHARACTERISTICS				
Current Gain – Bandwidth Product (Note 2) (I _C = 0.05 Adc, V _{CE} = 5.0 Vdc, f _{test} = 1.0 MHz)	f _T	6.0	_	MHz

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

^{2.} $f_T = |h_{fe}| \cdot f_{test}$.

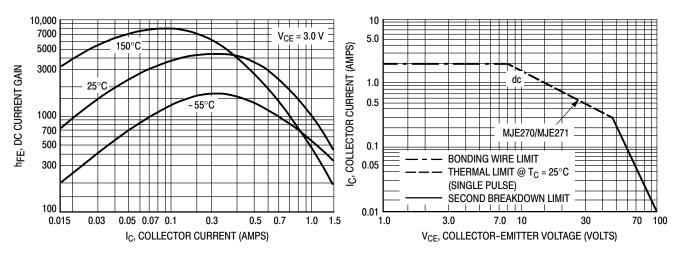


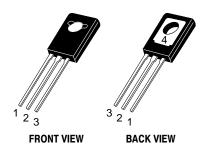
Figure 1. DC Current Gain

Figure 2. Safe Operating Area

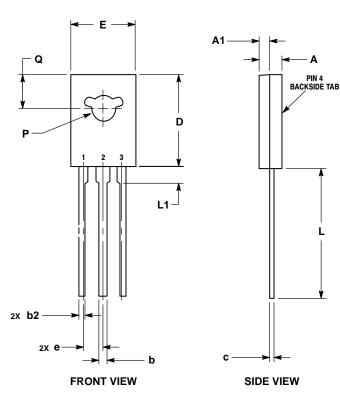
Pulse Test: Pulse Width ≤ 300 µs, Duty Cycle ≤ 2.0%.

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PACKAGE DIMENSIONS



TO-225 CASE 77-09 ISSUE AC



- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
 2. CONTROLLING DIMENSION: MILLIMETERS.
- 3. NUMBER AND SHAPE OF LUGS OPTIONAL.

	MILLIMETERS		
DIM	MIN	MAX	
Α	2.40	3.00	
A1	1.00	1.50	
b	0.60	0.90	
b2	0.51	0.88	
С	0.39	0.63	
D	10.60	11.10	
Е	7.40	7.80	
е	2.04	2.54	
L	14.50	16.63	
L1	1.27	2.54	
Р	2.90	3.30	
Q	3.80	4.20	

STYLE 3:

PIN 1.

COLLECTOR EMITTER

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