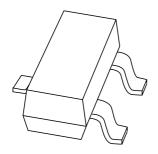
DISCRETE SEMICONDUCTORS

DATA SHEET



BC849; BC850 NPN general purpose transistors

Product specification Supersedes data of 1999 Apr 08 2004 Jan 16





Philips Semiconductors

NPN general purpose transistors

BC849; BC850

FEATURES

- Low current (max. 100 mA)
- Low voltage (max. 45 V).

APPLICATIONS

• General purpose switching and amplification.

DESCRIPTION

NPN transistor in a SOT23 plastic package. PNP complements: BC859 and BC860.

MARKING

TYPE NUMBER	MARKING CODE ⁽¹⁾	TYPE NUMBER	MARKING CODE ⁽¹⁾	
BC849B	2B*	BC850B	2F*	
BC849C	2C*	BC850C	2G*	

Note

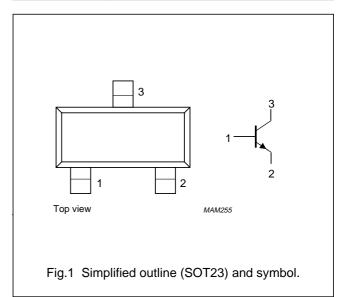
1. * = p: Made in Hong Kong.

* = t : Made in Malaysia.

* = W : Made in China.

PINNING

PIN	DESCRIPTION	
1	base	
2	emitter	
3	collector	



ORDERING INFORMATION

TYPE	PACKAGE			
NUMBER	NAME	DESCRIPTION	VERSION	
BC849B	_	plastic surface mounted package; 3 leads	SOT23	
BC849C				
BC850B				
BC850C				

NPN general purpose transistors

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LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter			
	BC849		_	30	V
	BC850		_	50	V
V _{CEO}	collector-emitter voltage	open base			
	BC849		_	30	V
	BC850		_	45	V
V _{EBO}	emitter-base voltage	open collector	_	5	V
I _C	collector current (DC)		_	100	mA
I _{CM}	peak collector current		_	200	mA
I _{BM}	peak base current		_	200	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C; note 1	_	250	mW
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature		_	150	°C
T _{amb}	operating ambient temperature		-65	+150	°C

Note

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th(j-a)}	thermal resistance from junction to ambient	note 1	500	K/W

Note

1. Transistor mounted on an FR4 printed-circuit board.

^{1.} Transistor mounted on an FR4 printed-circuit board.

NPN general purpose transistors

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CHARACTERISTICS

 $T_j = 25$ °C unless otherwise specified.

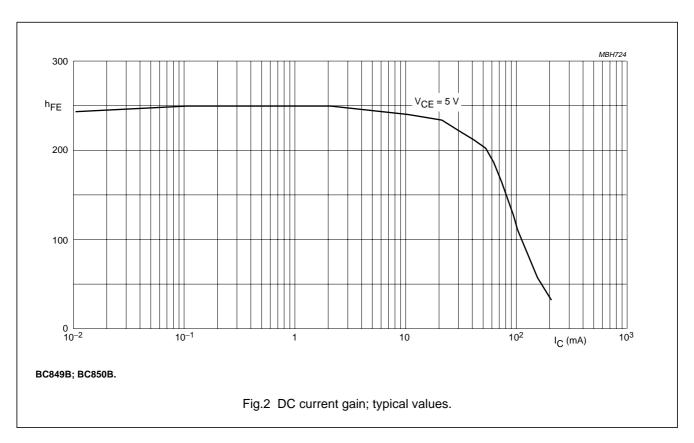
SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I _{CBO}	collector cut-off current	I _E = 0; V _{CB} = 30 V	_	_	15	nA
		I _E = 0; V _{CB} = 30 V; T _j = 150 °C	_	_	5	μΑ
I _{EBO}	emitter cut-off current	I _C = 0; V _{EB} = 5 V	_	_	100	nA
h _{FE}	DC current gain BC849B; BC850B	I_C = 10 μ A; V_{CE} = 5 V; see Figs 2 and 3	_	240	_	
	BC849C; BC850C		_	450	_	
	DC current gain	I _C = 2 mA; V _{CE} = 5 V;				
	BC849B; BC850B	see Figs 2 and 3	200	290	450	
	BC849C; BC850C		420	520	800	
V _{CEsat}	collector-emitter saturation	$I_C = 10 \text{ mA}; I_B = 0.5 \text{ mA}$	_	90	250	mV
voltag	voltage	I _C = 100 mA; I _B = 5 mA	_	200	600	mV
V_{BEsat}	base-emitter saturation voltage	$I_C = 10 \text{ mA}$; $I_B = 0.5 \text{ mA}$; note 1	_	700	_	mV
		$I_C = 100 \text{ mA}$; $I_B = 5 \text{ mA}$; note 1	_	900	_	mV
V_{BE}	base-emitter voltage	$I_C = 2 \text{ mA}$; $V_{CE} = 5 \text{ V}$; note 2	580	660	700	mV
		$I_C = 10 \text{ mA}; V_{CE} = 5 \text{ V}; \text{ note 2}$	_	_	770	mV
C _c	collector capacitance	$I_E = i_e = 0$; $V_{CB} = 10 \text{ V}$; $f = 1 \text{ MHz}$	_	2.5	_	pF
C _e	emitter capacitance	$I_C = I_c = 0$; $V_{EB} = 500 \text{ mV}$; $f = 1 \text{ MHz}$	_	11	_	pF
f_{T}	transition frequency	$I_C = 10 \text{ mA}; V_{CE} = 5 \text{ V}; f = 100 \text{ MHz}$	100	_	_	MHz
F	noise figure	I_C = 200 μA; V_{CE} = 5 V; R_S = 2 kΩ; f = 10 Hz to 15.7 kHz	_	_	4	dB
		I_C = 200 μA; V_{CE} = 5 V; R_S = 2 kΩ; f = 1 kHz; B = 200 Hz	_	_	4	dB

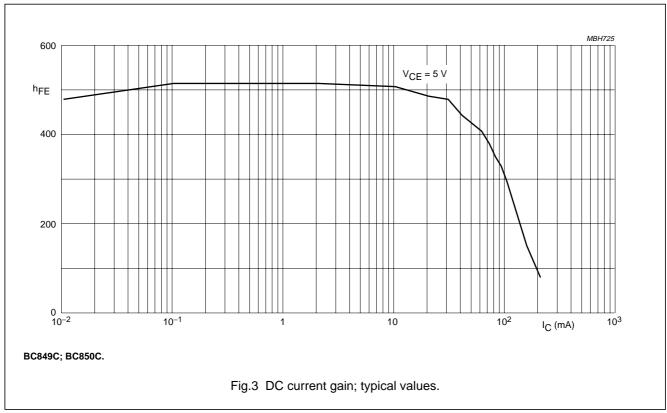
Notes

- 1. V_{BEsat} decreases by about 1.7 mV/K with increasing temperature.
- 2. V_{BE} decreases by about 2 mV/K with increasing temperature.

NPN general purpose transistors

BC849; BC850





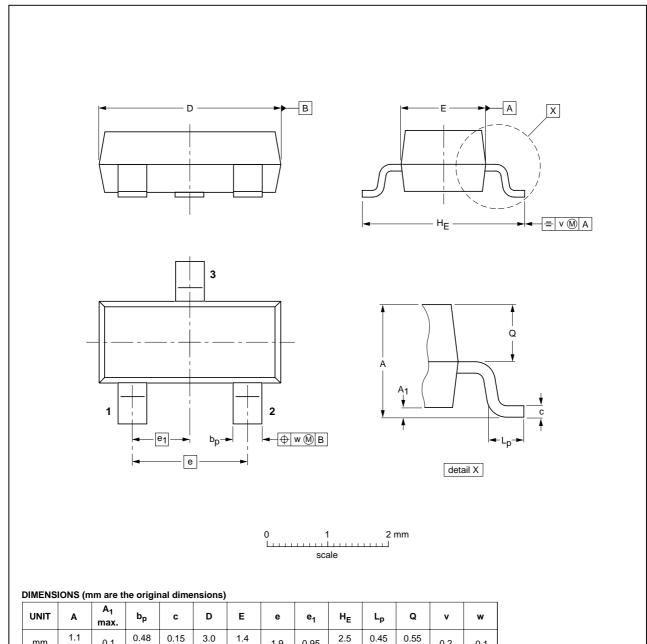
NPN general purpose transistors

BC849; BC850

PACKAGE OUTLINE

Plastic surface mounted package; 3 leads

SOT23



OUTLINE	REFERENCES			EUROPEAN	ISSUE DATE	
VERSION	IEC	JEDEC	EIAJ		PROJECTION	ISSUE DATE
SOT23		TO-236AB				-97-02-28 99-09-13

0.2

0.1

0.95

1.9

2004 Jan 16 6

0.9

NPN general purpose transistors

BC849; BC850

DATA SHEET STATUS

LEVEL	DATA SHEET STATUS ⁽¹⁾	PRODUCT STATUS(2)(3)	DEFINITION
I	Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.
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Notes

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- 3. For data sheets describing multiple type numbers, the highest-level product status determines the data sheet status.

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Limiting values definition — Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 60134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

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