Silicon Diffused Power Transistor BU2527AF

GENERAL DESCRIPTION

New generation, high-voltage, high-speed switching npn transistor in a plastic full-pack envelope intended for use in horizontal deflection circuits of high resolution monitors. Features improved RBSOA performance and is suitable for operation up to 64 kHz.

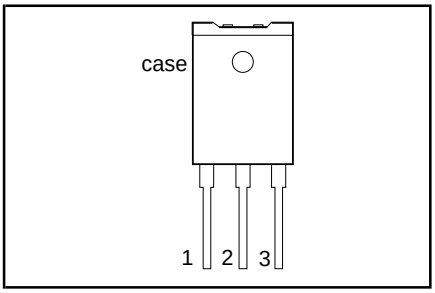
QUICK REFERENCE DATA

SYMBOL	PARAMETER	CONDITIONS	TYP.	MAX.	UNIT
V_{CESM}	Collector-emitter voltage peak value	V_{BE} = 0 V	-	1500	V
V_{CEO}	Collector-emitter voltage (open base)		-	800	V
I_C	Collector current (DC)		-	12	A
I_{CM}	Collector current peak value		-	30	A
P_{tot}	Total power dissipation	$T_{hs} \leq 25$ ˚C	-	45	W
V_{CEsat}	Collector-emitter saturation voltage	I_C = 6.0 A; I_B = 1.2 A	-	5.0	V
I_{Csat}	Collector saturation current		6.0	-	A
t_s	Storage time	I_{Csat} = 6.0 A; $I_{B(end)}$ = 0.55 A	1.7	2.0	µs

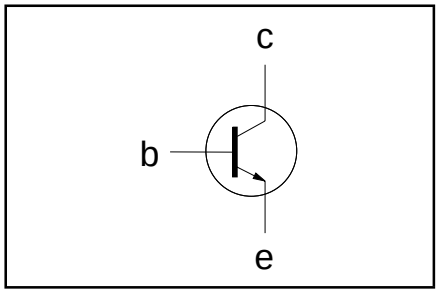
PINNING - SOT199

PIN	DESCRIPTION
1	base
2	collector
3	emitter
case	isolated

PIN CONFIGURATION



SYMBOL



LIMITING VALUES

Limiting values in accordance with the Absolute Maximum Rating System (IEC 134)

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V_{CESM}	Collector-emitter voltage peak value	V_{BE} = 0 V	-	1500	V
V_{CEO}	Collector-emitter voltage (open base)		-	800	V
I_C	Collector current (DC)		-	12	A
I_{CM}	Collector current peak value		-	30	A
I_B	Base current (DC)		-	8	A
I_{BM}	Base current peak value		-	12	A
$-I_{B(AV)}$	Reverse base current	average over any 20 ms period	-	200	mA
$-I_{BM}$	Reverse base current peak value [1]		-	7	A
P_{tot}	Total power dissipation	$T_{hs} \leq$ 25 ˚C	-	45	W
T_{stg}	Storage temperature		-65	150	˚C
T_j	Junction temperature		-	150	˚C

THERMAL RESISTANCES

SYMBOL	PARAMETER	CONDITIONS	TYP.	MAX.	UNIT
$R_{th\ j\text{-}hs}$	Junction to heatsink	without heatsink compound	-	3.7	K/W
$R_{th\ j\text{-}hs}$	Junction to heatsink	with heatsink compound	-	2.8	K/W
$R_{th\ j\text{-}a}$	Junction to ambient	in free air	35	-	K/W

1 Turn-off current.

ISOLATION LIMITING VALUE & CHARACTERISTIC

T_{hs} = 25 ˚C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V_{isol}	Repetitive peak voltage from all three terminals to external heatsink	R.H. ≤ 65 % ; clean and dustfree	-		2500	V
C_{isol}	Capacitance from T2 to external heatsink	f = 1 MHz	-	22	-	pF

STATIC CHARACTERISTICS

T_{hs} = 25 ˚C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I_{CES}	Collector cut-off current [2]	V_{BE} = 0 V; V_{CE} = $V_{CESMmax}$	-	-	0.25	mA
I_{CES}		V_{BE} = 0 V; V_{CE} = $V_{CESMmax}$; T_j = 125 ˚C	-	-	2.0	mA
I_{EBO}	Emitter cut-off current	V_{EB} = 7.5 V; I_C = 0 A	-	-	0.25	mA
BV_{EBO}	Emitter-base breakdown voltage	I_B = 1 mA	7.5	13.5	-	V
$V_{CEOsust}$	Collector-emitter sustaining voltage	I_B = 0 A; I_C = 100 mA; L = 25 mH	800	-	-	V
V_{CEsat}	Collector-emitter saturation voltage	I_C = 6.0 A; I_B = 1.2 A	-	-	5.0	V
V_{BEsat}	Base-emitter saturation voltage	I_C = 6.0 A; I_B = 1.2 A	-	-	1.3	V
h_{FE}	DC current gain	I_C = 1 A; V_{CE} = 5 V	-	10	-	
h_{FE}		I_C = 6 A; V_{CE} = 5 V	5	7	9	

DYNAMIC CHARACTERISTICS

T_{hs} = 25 ˚C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	TYP.	MAX.	UNIT
C_c	Collector capacitance	I_E = 0 A; V_{CB} = 10 V; f = 1 MHz	145	-	pF
	Switching times (64 kHz line deflection circuit)	I_{Csat} = 6.0 A; L_C = 170 µH; C_{fb} = 5.4 nF; $I_{B(end)}$ = 0.55 A; L_B = 0.6 µH; $-V_{BB}$ = 2 V; ($-dI_B/dt$ = 3.33 A/µs)			
t_s	Turn-off storage time		1.7	2.0	µs
t_f	Turn-off fall time		0.1	0.2	µs

2 Measured with half sine-wave voltage (curve tracer).

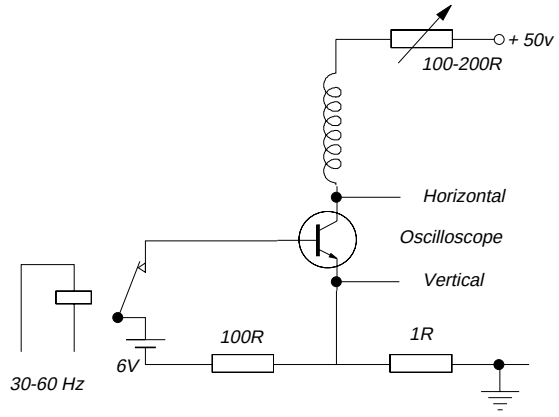


Fig.1. Test circuit for $V_{CEOsust}$.

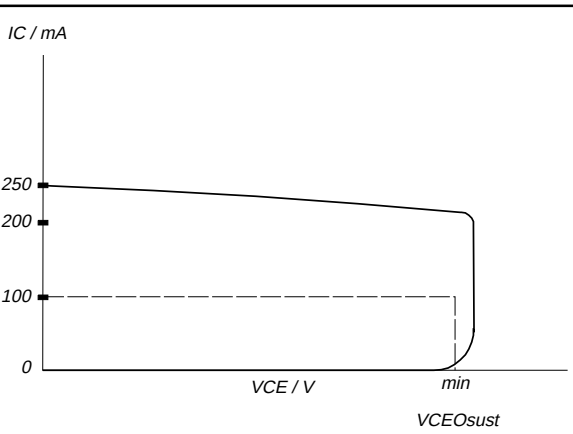


Fig.2. Oscilloscope display for $V_{CEOsust}$.

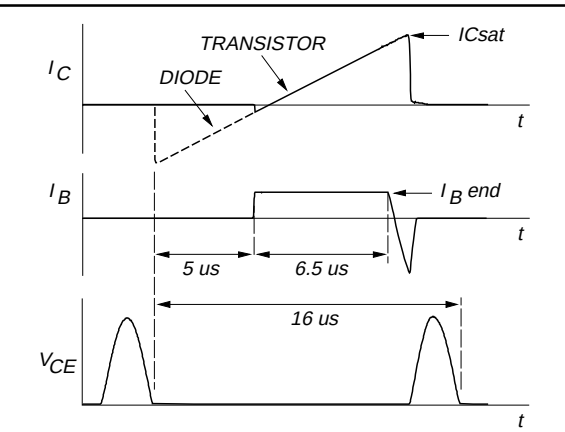


Fig.3. Switching times waveforms.

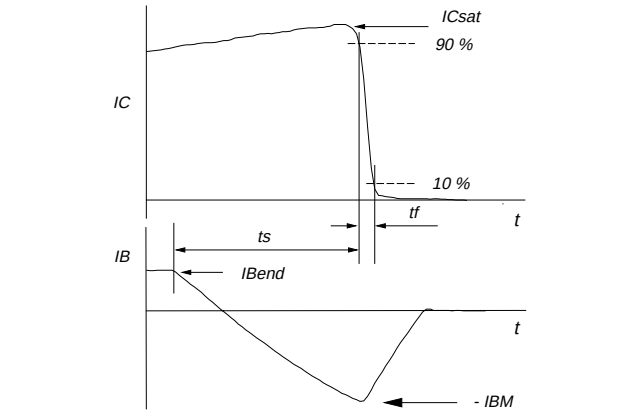


Fig.4. Switching times definitions.

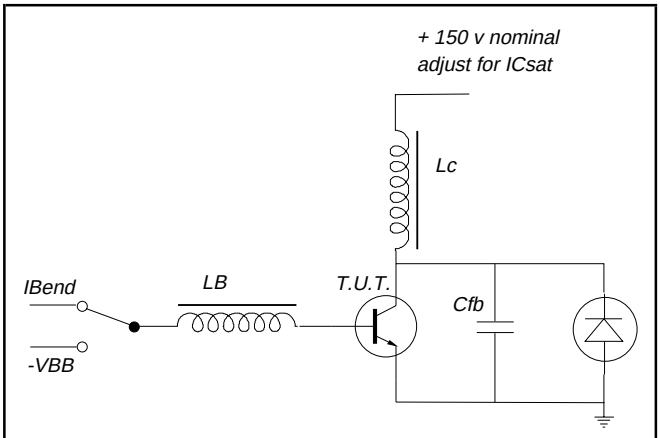


Fig.5. Switching times test circuit.

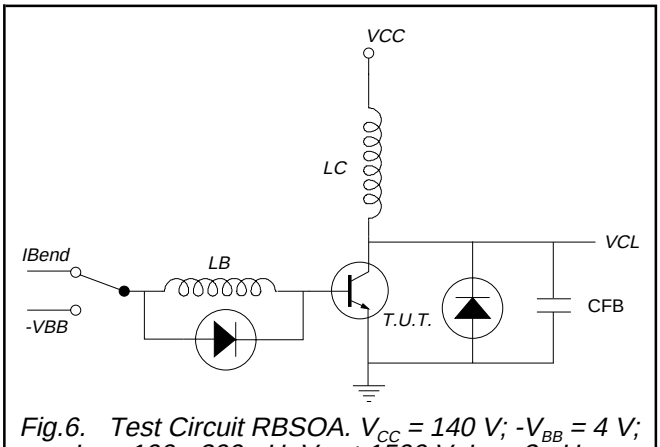


Fig.6. Test Circuit RBSOA. V_{CC} = 140 V; -V_{BB} = 4 V; L_C = 100 - 200 µH; V_{CL} ≤ 1500 V; L_B = 3 µH; C_{FB} = 1 - 2.2 nF; I_B(end) = 1 - 2 A

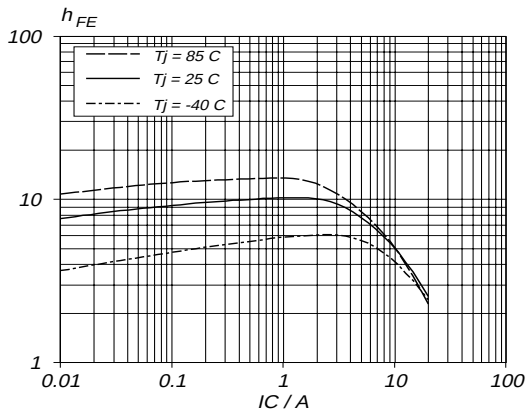


Fig.7. Typical DC current gain. $h_{FE} = f(I_C)$ $V_{CE} = 5$ V

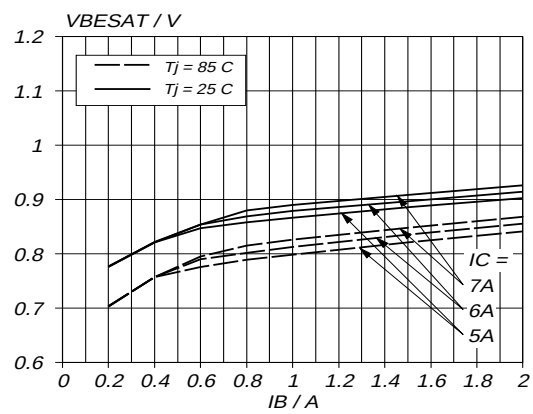


Fig.10. Typical base-emitter saturation voltage. $V_{BE}sat = f(I_B)$; parameter I_C

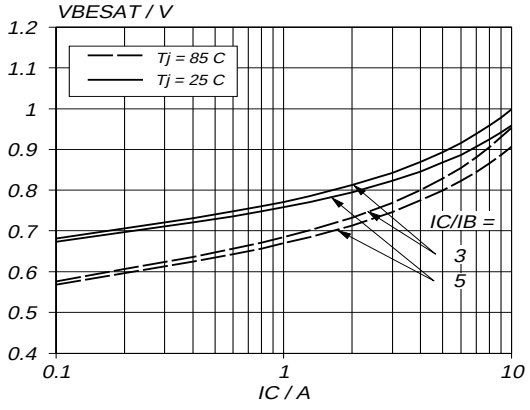


Fig.8. Typical base-emitter saturation voltage. $V_{BE}sat = f(I_C)$; parameter I_C/I_B

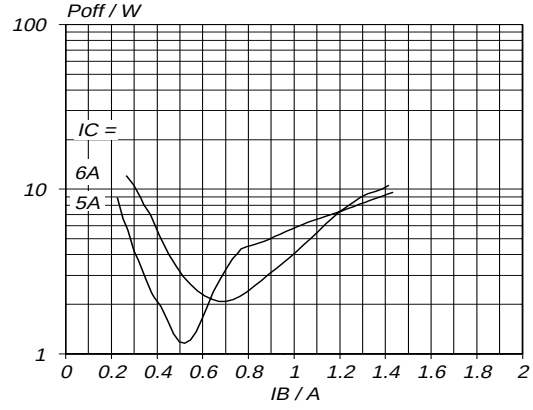


Fig.11. Typical turn-off losses. $T_j = 85^{\circ}C$ Poff = $f(I_B)$; parameter I_C; f = 64 kHz

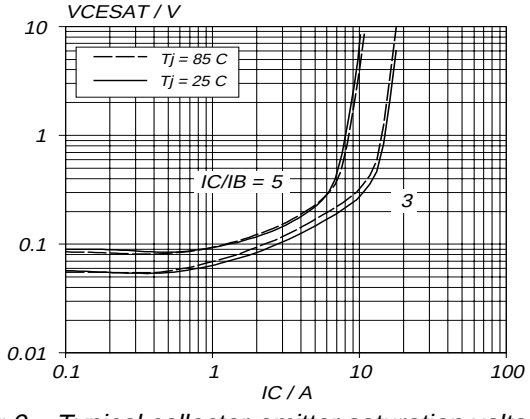


Fig.9. Typical collector-emitter saturation voltage. $V_{CE}sat = f(I_C)$; parameter I_C/I_B

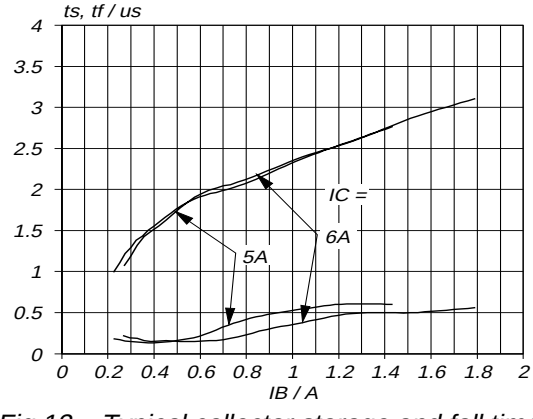


Fig.12. Typical collector storage and fall time. ts = $f(I_B)$; tf = $f(I_B)$; parameter I_C; $T_j = 85^{\circ}C$; f = 64 kHz

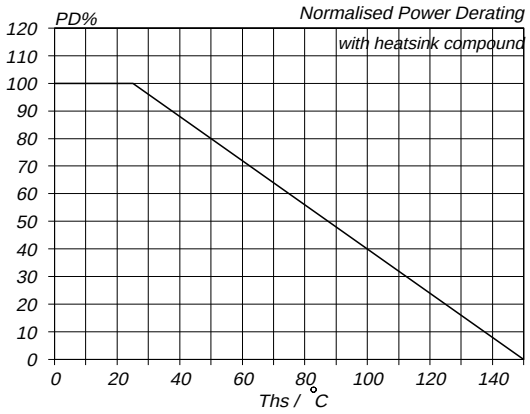


Fig.13. Normalised power dissipation.
PD% = 100·P_D/$P_{D\ 25°C}$ = f (T_{hs})

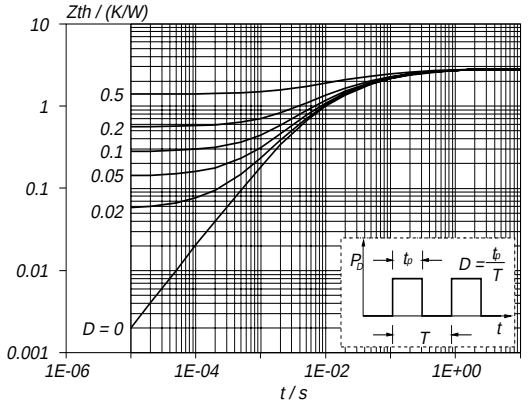


Fig.14. Transient thermal impedance.
$Z_{th\ j\text{-}hs}$ = f(t); parameter D = t_p/T

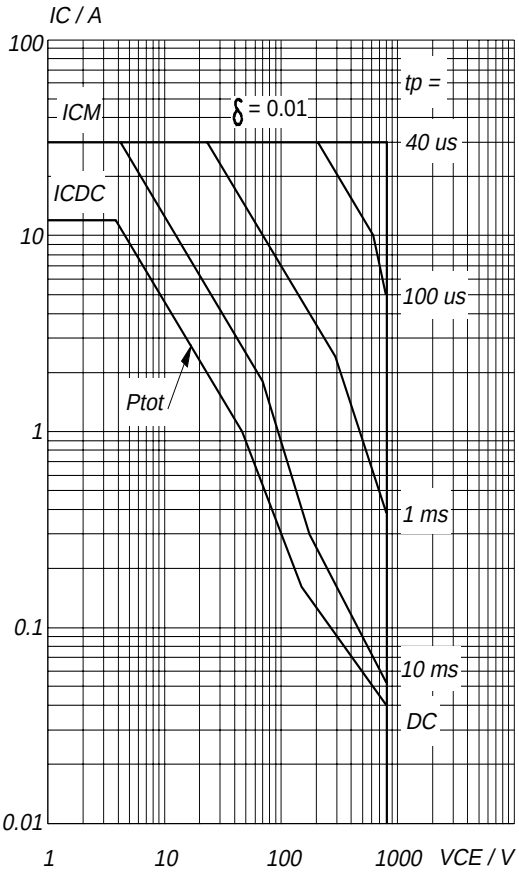


Fig.15. Forward bias safe operating area. T_{hs} = 25 ˚C
I_{CDC} & I_{CM} = f(V_{CE}); I_{CM} single pulse; parameter t_p
Second-breakdown limits independant of temperature.
Mounted with heatsink compound.

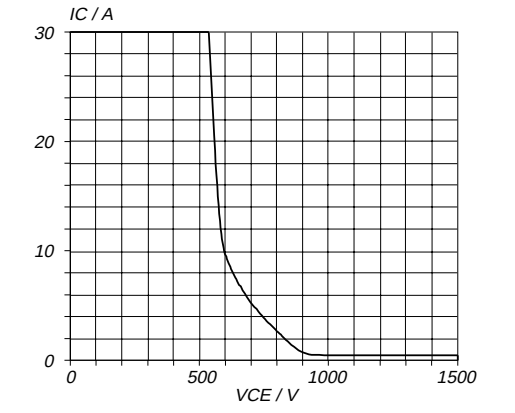


Fig.16. Reverse bias safe operating area. $T_j \leq T_{jmax}$

MECHANICAL DATA

Dimensions in mm

Net Mass: 5.5 g

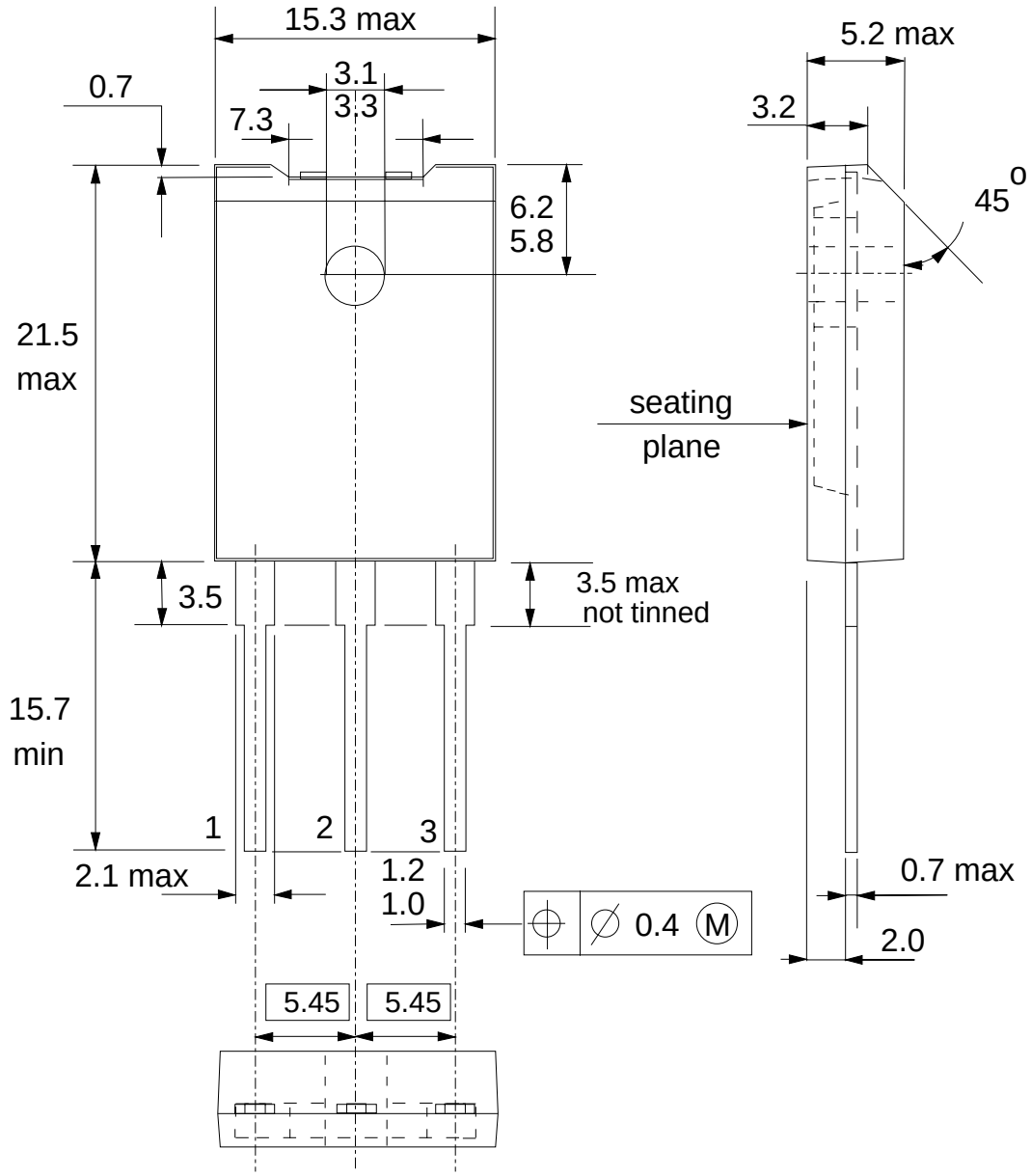


Fig.17. SOT199; The seating plane is electrically isolated from all terminals.

Notes
1. Refer to mounting instructions for F-pack envelopes.
2. Epoxy meets UL94 V0 at 1/8".