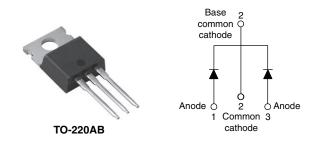


VS-48CTQ060PbF, VS-48CTQ060-N3

Vishay Semiconductors

Schottky Rectifier, 2 x 20 A



PRODUCT SUMMARY				
Package	TO-220AB			
I _{F(AV)}	2 x 20 A			
V _R	60 V			
V _F at I _F	0.58 V			
I _{RM} max.	89 mA at 125 °C			
T _J max.	150 °C			
Diode variation	Common cathode			
E _{AS}	13 mJ			

FEATURES

- Low forward voltage drop
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance



RoHS

COMPLIANT

HALOGEN

FREE

- Guard ring for enhanced ruggedness and long term reliability
- 150 °C T_J operation
- High frequency operation
- Compliant to RoHS Directive 2002/95/EC
- Designed and qualified according to JEDEC-JESD47
- Halogen-free according to IEC 61249-2-21 definition (-N3 only)

DESCRIPTION

This center tap Schottky rectifier has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL CHARACTERISTICS VALUES UNITS						
I _{F(AV)}	Rectangular waveform	40	А			
V _{RRM}		60	V			
I _{FSM}	t _p = 5 μs sine	1000	А			
V _F	20 A _{pk} , T _J = 125 °C (per leg)	0.58	V			
TJ	Range	- 55 to 150	°C			

VOLTAGE RATINGS						
PARAMETER	SYMBOL	VS-48CTQ060PbF	VS-48CTQ060-N3	UNITS		
Maximum DC reverse voltage	V _R	60	60	V		
Maximum working peak reverse voltage	V _{RWM}	00	00	v		

ABSOLUTE MAXIMUM RATINGS							
PARAMETER		SYMBOL	TEST CONDITIONS		VALUES	UNITS	
Maximum average	per leg	I =	50 % duty cycle at T_{C} = 111 °C, rectangular waveform		20		
	device	I _{F(AV)}			40	Α	
Maximum peak one cycle non-repetitive surge current per legI I FSMSee fig. 7			Following any rated load condition and with	1000			
		IFSM	10 ms sine or 6 ms rect. pulse	rated V_{RRM} applied	260		
Non-repetitive avalanche energy per leg		E _{AS}	T _J = 25 °C, I _{AS} = 1.50 A, L = 11.5 mH		13	mJ	
Repetitive avalanche current per leg		I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		1.50	А	

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ELECTRICAL SPECIFICATIONS

PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
		20 A	T.I = 25 °C	0.61	V
Maximum forward voltage drop per leg	V _{FM} ⁽¹⁾	40 A	1j=25 C	0.83	
See fig. 1	VFM (1)	20 A	T.I = 125 °C	0.58	
		40 A	$1_{\rm J} = 125$ C	0.75	
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	V _R = Rated V _R	2	mA
See fig. 2		T _J = 125 °C		89	
Threshold voltage	V _{F(TO)}			0.37	V
Forward slope resistance	r _t	$T_J = T_J maximum$		8.26	mΩ
Maximum junction capacitance per leg	CT	V_R = 5 V_{DC} (test signal range 100 kHz to 1 MHz), 25 °C		1220	pF
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body		8.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs

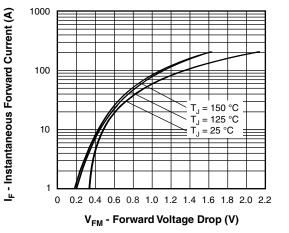
Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range		T _J , T _{Stg}		- 55 to 150	°C	
Maximum thermal resistance, junction to case per leg		P		2.0		
Maximum thermal resistance, junction to case per package		R _{thJC}	DC operation	1.0	°C/W	
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.50		
Approximate weight				2	g	
Approximate weight				0.07	oz.	
Mounting torque	minimum			6 (5)	kgf · cm	
Mounting torque	maximum			12 (10)	(lbf · in)	
Marking device Case style TO-220AB 48CTQ060				Q060		

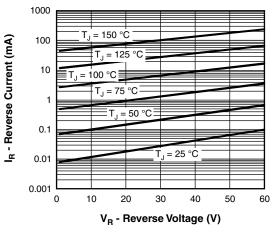
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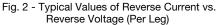
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Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)





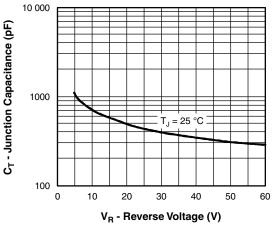


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

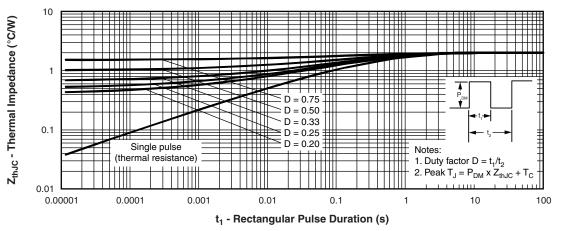


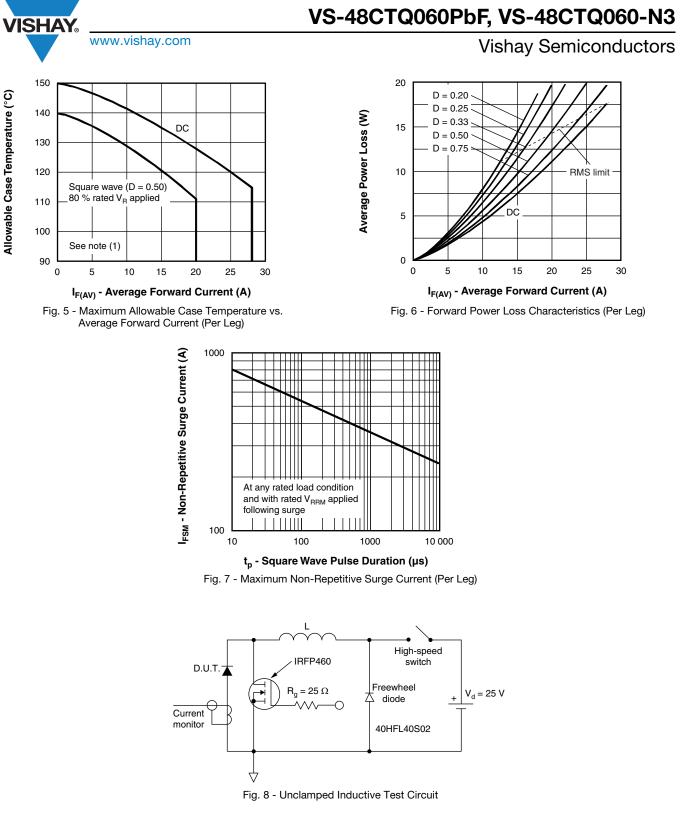
Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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Note

- ⁽¹⁾ Formula used: $T_C = T_J (Pd + Pd_{REV}) \times R_{thJC}$;
- Pd = Forward power loss = $I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6);
- Pd_{REV} = Inverse power loss = $V_{R1} \times I_R (1 D)$; $I_R \text{ at } V_{R1} = 10 \text{ V}$

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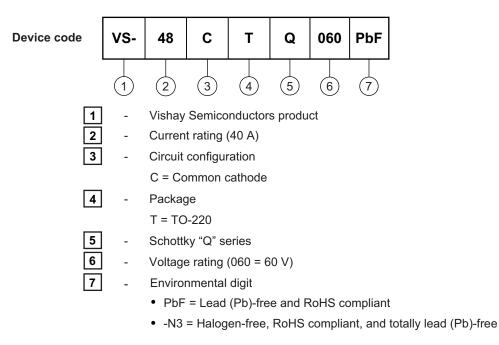
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VS-48CTQ060PbF, VS-48CTQ060-N3

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ORDERING INFORMATION TABLE



ORDERING INFORMATION (Example)					
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION		
VS-48CTQ060PbF	50	1000	Antistatic plastic tube		
VS-48CTQ060-N3	50	1000	Antistatic plastic tube		

LINKS TO RELATED DOCUMENTS					
Dimensions www.vishay.com/doc?95222					
Part marking information	TO-220AB PbF	www.vishay.com/doc?95225			
	TO-220AB -N3	www.vishay.com/doc?95028			
SPICE model		www.vishay.com/doc?95424			



Vishay Semiconductors

TO-220AB

DIMENSIONS in millimeters and inches





.ead	assignments

Diodes

1. - Anode/open 2. - Cathode 3. - Anode

SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.25	4.65	0.167	0.183	
A1	1.14	1.40	0.045	0.055	
A2	2.56	2.92	0.101	0.115	
b	0.69	1.01	0.027	0.040	
b1	0.38	0.97	0.015	0.038	4
b2	1.20	1.73	0.047	0.068	
b3	1.14	1.73	0.045	0.068	4
С	0.36	0.61	0.014	0.024	
c1	0.36	0.56	0.014	0.022	4
D	14.85	15.25	0.585	0.600	3
D1	8.38	9.02	0.330	0.355	
D2	11.68	12.88	0.460	0.507	6

Notes

- ⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994
- ⁽²⁾ Lead dimension and finish uncontrolled in L1
- ⁽³⁾ Dimension D, D1 and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- $^{\left(4\right) }$ Dimension b1, b3 and c1 apply to base metal only
- (5) Controlling dimensions: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2 and E1

MILLIMETERS INCHES SYMBOL NOTES MIN. MAX. MIN. MAX. 10.51 0.414 10.11 0.398 3,6 Е E1 6.86 8.89 0.270 0.350 6 E2 0.76 0.030 7 --2.41 2.67 0.095 0.105 е 0.208 e1 4.88 5.28 0.192 H1 6.09 6.48 0.240 0.255 6,7 13.52 14.02 0.532 0.552 L L1 3.32 3.82 0.131 0.150 2 ØΡ 3.54 3.73 0.139 0.147 2.60 0.102 Q 3.00 0.118 90° to 93° 90° to 93° θ

Conforms to JEDEC outline TO-220AB

- (7) Dimensions E2 x H1 define a zone where stamping and singulation irregularities are allowed
- (8) Outline conforms to JEDEC TO-220, except A2 (maximum) and D2 (minimum) where dimensions are derived from the actual package outline



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