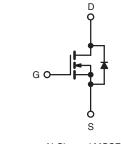


Vishay Siliconix

Power MOSFET

PRODUCT SUMMARY						
V _{DS} (V)	200					
R _{DS(on)} (Ω)	V _{GS} = 10 V 0.18					
Q _g (Max.) (nC)	70					
Q _{gs} (nC)	13					
Q _{gd} (nC)	39					
Configuration	Single					





N-Channel MOSFET

FEATURES

- Dynamic dV/dt Rating
- Repetitive Avalanche Rated
- · Fast Switching
- · Ease of Paralleling
- Simple Drive Requirements
- Compliant to RoHS Directive 2002/95/EC

DESCRIPTION

Third generation Power MOSFETs from Vishay provide the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost-effectiveness.

The TO-220AB package is universally preferred for all commercial-industrial applications at power dissipation levels to approximately 50 W. The low thermal resistance and low package cost of the TO-220AB contribute to its wide acceptance throughout the industry.

ORDERING INFORMATION	
Package	TO-220AB
Lead (Pb)-free	IRF640PbF
	SiHF640-E3
SnPb	IRF640
SIIFD	SiHF640

ABSOLUTE MAXIMUM RATINGS (T _C	= 25 °C, unl	ess otherwis	se noted)			
PARAMETER			SYMBOL	LIMIT	UNIT	
Drain-Source Voltage			V _{DS}	200	v	
Gate-Source Voltage			V _{GS}	± 20	v	
Continuous Drain Current	V _{GS} at 10 V	T _C = 25 °C	- I _D	18		
Continuous Drain Current		$T_C = 100 \ ^\circ C$		11	А	
Pulsed Drain Current ^a			I _{DM}	72		
Linear Derating Factor				1.0	W/°C	
Single Pulse Avalanche Energy ^b			E _{AS}	580	mJ	
Repetitive Avalanche Current ^a			I _{AR}	18	А	
Repetitive Avalanche Energy ^a			E _{AR}	13	mJ	
Maximum Power Dissipation	T _C = 25 °C		P _D	125	W	
Peak Diode Recovery dV/dt ^c			dV/dt	5.0	V/ns	
Operating Junction and Storage Temperature Range			T _J , T _{stg}	- 55 to + 150		
Soldering Recommendations (Peak Temperature)	for 10 s			300 ^d		
Mounting Torque	6-32 or M3 screw			10	lbf ⋅ in	
			F	1.1	N·m	

Notes

a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11).

b. $V_{DD} = 50 \text{ V}$, starting $T_J = 25 \text{ °C}$, L = 2.7 mH, $R_g = 25 \Omega$, $I_{AS} = 18 \text{ A}$ (see fig. 12). c. $I_{SD} \le 18 \text{ A}$, dI/dt $\le 150 \text{ A/}\mu\text{s}$, $V_{DD} \le V_{DS}$, $T_J \le 150 \text{ °C}$.

d. 1.6 mm from case.

* Pb containing terminations are not RoHS compliant, exemptions may apply

Document Number: 91036 S11-0509-Rev. B, 21-Mar-11 www.vishay.com

BoHS COMPLIANT

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THERMAL RESISTANCE RATI	NGS							
PARAMETER	SYMBOL	TYP.		MAX.			UNIT	
Maximum Junction-to-Ambient	R _{thJA}	-		62				
Case-to-Sink, Flat, Greased Surface	R _{thCS}	0.50 -			°C/W			
Maximum Junction-to-Case (Drain)	R _{thJC}	-		1.0				
SPECIFICATIONS (T _J = 25 $^{\circ}$ C, u	nless otherw	ise noted)						
PARAMETER	SYMBOL	TEST	CONDITI	ONS	MIN.	TYP.	MAX.	UNIT
Static								
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0$	V, I _D = 2	50 µA	200	-	-	V
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	Reference t	o 25 °C,	I _D = 1 mA	-	0.29	-	V/°C
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V$	_{GS} , I _D = 2	50 µA	2.0	-	4.0	V
Gate-Source Leakage	I _{GSS}	V _G	s = ± 20 \	/	-	-	± 100	nA
Zovo Coto Voltogo Droin Current		V _{DS} = 200 V, V _{GS} = 0 V		-	-	25		
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 160 V, V	′ _{GS} = 0 V,	T _J = 125 °C	-	-	250	μA
Drain-Source On-State Resistance	R _{DS(on)}	$V_{GS} = 10 V$	ار	₀ = 11 A ^b	-	-	0.18	Ω
Forward Transconductance	9 _{fs}	V _{DS} = 5	0 V, I _D =	11 A ^b	6.7	-	-	S
Dynamic								
Input Capacitance	C _{iss}	$V_{ex} = 0 V_{ex}$			-	1300	-	
Output Capacitance	C _{oss}	V _{GS} = 0 V, V _{DS} = 25 V, f = 1.0 MHz, see fig. 5				430	-	pF
Reverse Transfer Capacitance	C _{rss}	f = 1.0	MHz, see	fig. 5	-	130	-	
Total Gate Charge	Qg				-	-	70	
Gate-Source Charge	Q _{gs}	$V_{GS} = 10 V$		A, V _{DS} =160 V, ig. 6 and 13 ^b	-	-	13	nC
Gate-Drain Charge	Q _{gd}		0001	ig. o una ro	-	-	39	
Turn-On Delay Time	t _{d(on)}		-		-	14	-	
Rise Time	t _r	V _{DD} = 1	00 V, I _D =	18 A,	-	51	-	
Turn-Off Delay Time	t _{d(off)}	$R_g = 9.1 \Omega$, $R_D = 5.4 \Omega$, see fig. 10^b		-	45	-	ns	
Fall Time	t _f				-	36	-	
Internal Drain Inductance	L _D	Between lead, 6 mm (0.25") from package and center of die contact - 36 - - 4.5 - - 7.5 -		-				
Internal Source Inductance	Ls			7.5	-	- nH		
Drain-Source Body Diode Characteristic	s							
Continuous Source-Drain Diode Current	I _S	MOSFET symbo showing the)I		-	-	18	А
Pulsed Diode Forward Current ^a	I _{SM}	p - n junction diode		72				
Body Diode Voltage	V_{SD}	T _J = 25 °C, I	_S = 18 A,	V _{GS} = 0 V ^b	-	-	2.0	V
Body Diode Reverse Recovery Time	t _{rr}	T _J = 25 °C, I _F =	18 A J//	1t - 100 A/us ^b	-	300	610	ns
Body Diode Reverse Recovery Charge	Q _{rr}	ij - 23 0, if =	10 A, ui/t	aι – 100 Αγμδ'	-	3.4	7.1	μC
Forward Turn-On Time	t _{on}	Intrinsic turn	on time i	s negligible (turn	-on is doi	minated b	by L _S and	L _D)
			-		-			

Notes

a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11).

b. Pulse width \leq 300 µs; duty cycle \leq 2 %.

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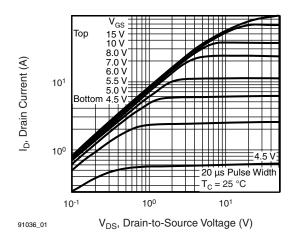


Fig. 1 - Typical Output Characteristics, T_C = 25 °C

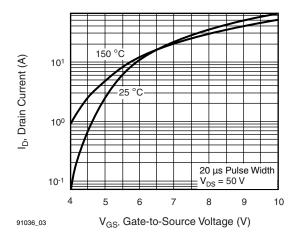


Fig. 3 - Typical Transfer Characteristics

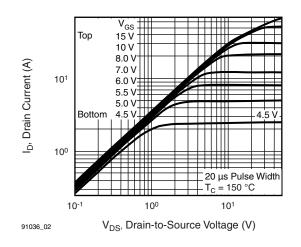


Fig. 2 - Typical Output Characteristics, T_C = 150 $^\circ C$

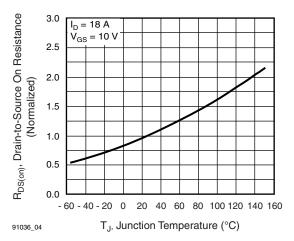


Fig. 4 - Normalized On-Resistance vs. Temperature

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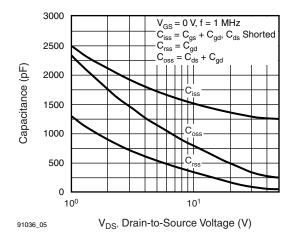


Fig. 5 - Typical Capacitance vs. Drain-to-Source Voltage

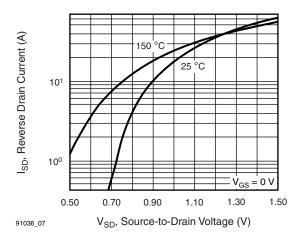


Fig. 7 - Typical Source-Drain Diode Forward Voltage

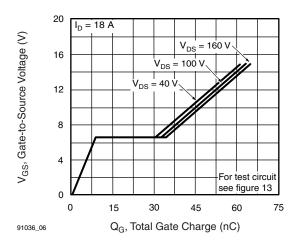


Fig. 6 - Typical Gate Charge vs. Gate-to-Source Voltage

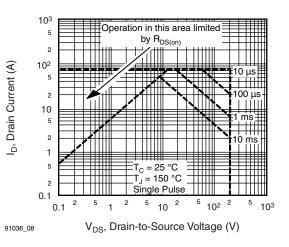


Fig. 8 - Maximum Safe Operating Area

Document Number: 91036 S11-0509-Rev. B, 21-Mar-11



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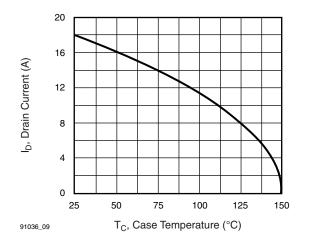


Fig. 9 - Maximum Drain Current vs. Case Temperature

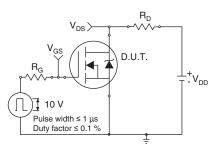


Fig. 10a - Switching Time Test Circuit

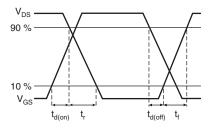


Fig. 10b - Switching Time Waveforms

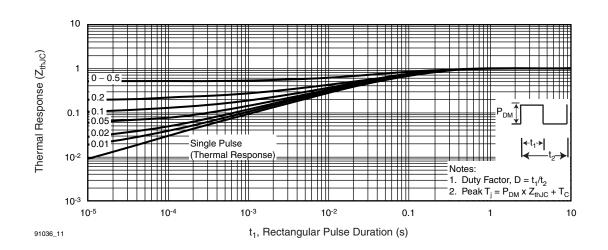


Fig. 11 - Maximum Effective Transient Thermal Impedance, Junction-to-Case

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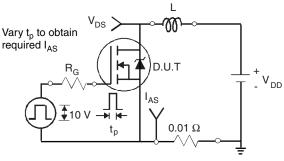


Fig. 12a - Unclamped Inductive Test Circuit

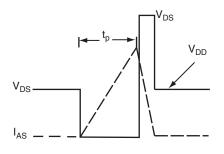


Fig. 12b - Unclamped Inductive Waveforms

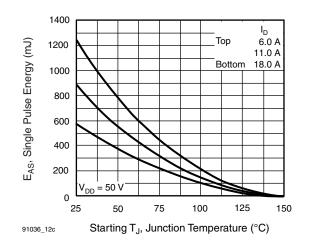


Fig. 12c - Maximum Avalanche Energy vs. Drain Current

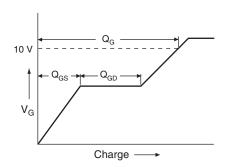


Fig. 13a - Basic Gate Charge Waveform

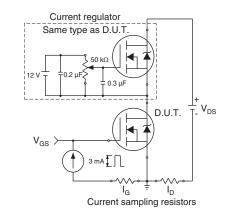
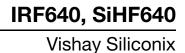


Fig. 13b - Gate Charge Test Circuit

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Peak Diode Recovery dV/dt Test Circuit

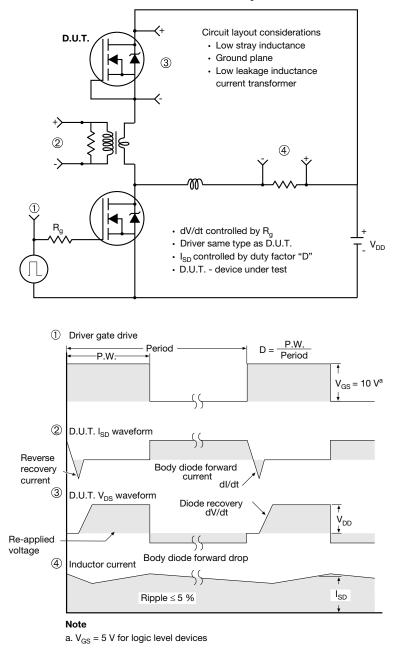


Fig. 14 - For N-Channel

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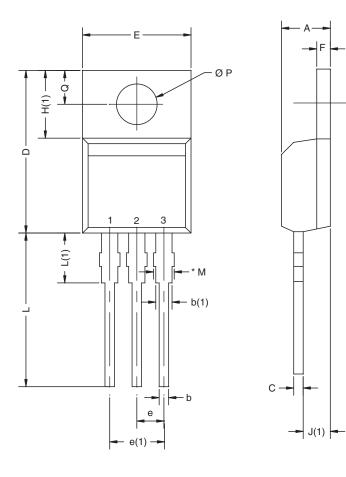
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Package Information

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TO-220AB



	MILLIMETERS		INC	CHES	
DIM.	MIN.	MAX.	MIN.	MAX.	
А	4.25	4.65	0.167	0.183	
b	0.69	1.01	0.027	0.040	
b(1)	1.20	1.73	0.047	0.068	
С	0.36	0.61	0.014	0.024	
D	14.85	15.49	0.585	0.610	
Е	10.04	10.51	0.395	0.414	
е	2.41	2.67	0.095	0.105	
e(1)	4.88	5.28	0.192	0.208	
F	1.14	1.40	0.045	0.055	
H(1)	6.09	6.48	0.240	0.255	
J(1)	2.41	2.92	0.095	0.115	
L	13.35	14.02	0.526	0.552	
L(1)	3.32	3.82	0.131	0.150	
ØΡ	3.54	3.94	0.139	0.155	
Q	2.60	3.00	0.102	0.118	
	0416-Rev. M,		0.102	0.11	

Note

 * M = 1.32 mm to 1.62 mm (dimension including protrusion) Heatsink hole for HVM



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