RoHS COMPLIANT

Vishay High Power Products

243NQ100PbF

Schottky Rectifier, 240 A



HALF-PAK (D-67)

PRODUCT SUMMARY

I_{F(AV)}

 V_{R}

6 Base cathode

240 A

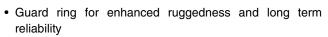
100 V

Lug terminal anode

С

FEATURES • 175 °C T_J operation

- · Low forward voltage drop
- · High frequency operation



- Lead (Pb)-free
- · Designed and qualified for industrial level

DESCRIPTION

The 243NQ.. high current Schottky rectifier module series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in high current switching power supplies, plating power supplies, UPS systems, converters, freewheeling diodes, welding, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	240	A		
V _{RRM}		100	V		
I _{FSM}	t _p = 5 μs sine	25 500	A		
V _F	240 Apk, T _J = 125 °C	0.72	V		
TJ	Range	- 55 to 175	°C		

VOLTAGE RATINGS				
PARAMETER	SYMBOL	243NQ100PbF	UNITS	
Maximum DC reverse voltage	V _R	100 V		
Maximum working peak reverse voltage	V _{RWM}	100	v	

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at T_C = 132 °C, rectangular waveform		240	
Maximum peak one cycle non-repetitive surge current	1	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with	25 500	A
See fig. 7	IFSM	10 ms sine or 6 ms rect. pulse	rated V_{RRM} applied	3300	
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 5.5 A, L = 1 mH		15	mJ
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		1	А



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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
	V _{FM} ⁽¹⁾	240 A	T _J = 25 °C	0.95	v
Maximum forward voltage drop		480 A		1.26	
See fig. 1		240 A	T _J = 125 °C	0.72	
		480 A		0.85	
Maximum reverse leakage current	I _{RM}	T _J = 25 °C	V _R = Rated V _R	6	mA
See fig. 2		T _J = 125 °C		80	
Maximum junction capacitance	CT	$V_{\rm R}$ = 5 $V_{\rm DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		5500	pF
Typical series inductance	L _S	From top of terminal hole to mounting plane		5.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs

Note

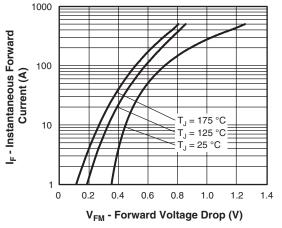
⁽¹⁾ Pulse width = 500 μ s

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage tem	perature range	T _J , T _{Stg}		- 55 to 175	°C	
Maximum thermal resistance, junction to case		R _{thJC}	DC operation See fig. 4	0.19	°C/W	
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.05	0/10	
Approximate weight				30	g	
				1.06	oz.	
Mounting torque	minimum			3 (26.5)		
Mounting torque	maximum		Non-lubricated threads	4 (35.4)	N ⋅ m (lbf ⋅ in)	
Terminal torque	minimum		Non-Iudricated threads	3.4 (30)		
Terminal torque	maximum			5 (44.2)		
Case style				HALF-PA	K module	



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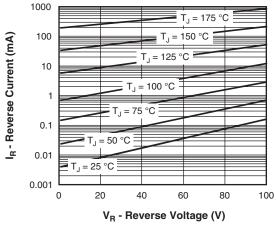


Fig. 1 - Maximum Forward Voltage Drop Characteristics

Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

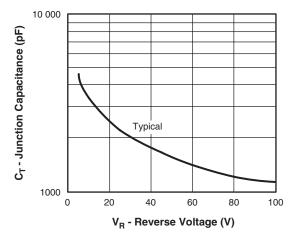
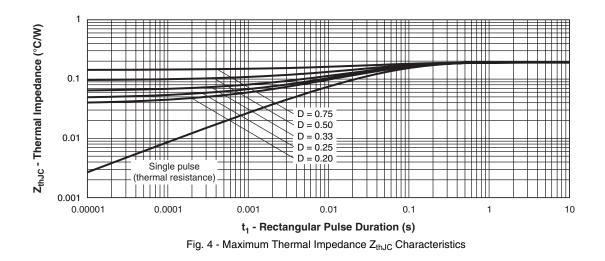
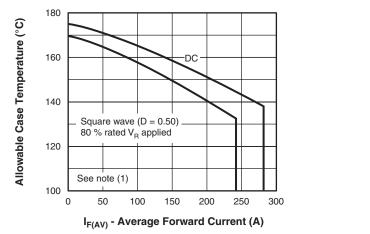


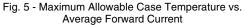
Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

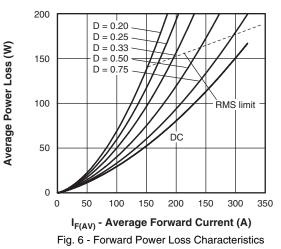


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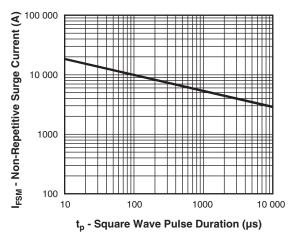
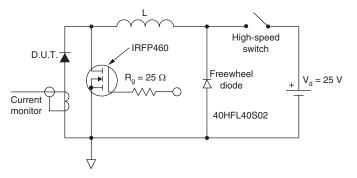


Fig. 7 - Maximum Non-Repetitive Surge Current





Note

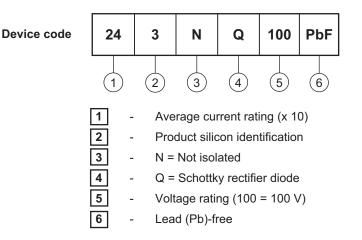
- ⁽¹⁾ Formula used: $T_C = T_J (Pd + Pd_{REV}) \times R_{thJC}$;
 - Pd = Forward power loss = $I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6); Pd_{REV} = Inverse power loss = $V_{R1} \times I_R$ (1 - D); I_R at V_{R1} = Rated V_R



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ORDERING INFORMATION TABLE



LINKS TO RELATED DOCUMENTS			
Dimensions	http://www.vishay.com/doc?95020		

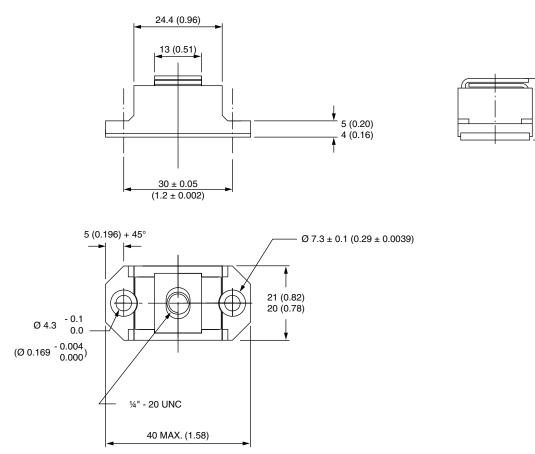
Vishay Semiconductors

17.5 (0.69) 16.5 (0.65)



DIMENSIONS in millimeters (inches)

SHAY





Vishay

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