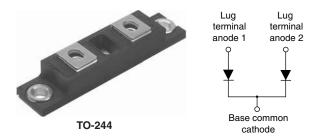


Vishay High Power Products

Schottky Rectifier, 300 A



PRODUCT SUMMARY				
I _{F(AV)}	300 A			
V_{R}	100 V			

FEATURES

- 175 °C T_J operation
- · Center tap module
- · Low forward voltage drop
- · High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Lead (Pb)-free
- Designed and qualified for industrial level

DESCRIPTION

The 303CNQ... center tap Schottky rectifier module series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in high current switching power supplies, plating power supplies, UPS systems, converters, freewheeling diodes, welding, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	300	A		
V _{RRM}		100	V		
I _{FSM}	t _p = 5 μs sine	22 000	A		
V _F	150 Apk, T _J = 125 °C (per leg)	0.72	V		
T _J	Range	- 55 to 175	°C		

VOLTAGE RATINGS				
PARAMETER	SYMBOL	303CNQ100PbF	UNITS	
Maximum DC reverse voltage	V _R	100	V	
Maximum working peak reverse voltage	V _{RWM}	100	V	

ABSOLUTE MAXIMUM RATINGS								
PARAMETER		SYMBOL	TEST CONDITIONS		VALUES	UNITS		
Maximum average forward current	per leg		I _{F(AV)} 50 % duty cycle at T _C = 138 °C, rectangular waveform		50 % duty puels at T 100 %C vector gular vacuators		150	
See fig. 5	per device	'F(AV)			300	_		
Maximum peak one cycle no surge current per leg	n-repetitive		5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated	22 000	- A		
See fig. 7		IFSM	10 ms sine or 6 ms rect. pulse	V _{RRM} applied	2500			
Non-repetitive avalanche ene	ergy per leg	E _{AS}	T _J = 25 °C, I _{AS} = 13 A, L = 0.2 mH		15	mJ		
Repetitive avalanche current	per leg	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		1	Α		

Document Number: 94177 Revision: 28-Apr-08

303CNQ100PbF

Vishay High Power Products Schottky Rectifier, 300 A



ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop per leg	V _{FM} ⁽¹⁾	150 A	T _{.1} = 25 °C	0.91	V
		300 A	1j = 25 C	1.09	
See fig. 1		150 A	T 105 °C	0.72	
		300 A	T _J = 125 °C	0.85	
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	$V_{\rm B}$ = Rated $V_{\rm B}$	4.5	mA
See fig. 2		T _J = 125 °C	VR = nateu VR	80	IIIA
Maximum junction capacitance per leg	C _T	V _R = 5 V _{DC} (test signal range 100 kHz to 1 MHz) 25 °C		4150	pF
Typical series inductance per leg	L _S	From top of terminal hole to mounting plane		6.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs

Note

 $^{^{(1)}\,}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNITS	
Maximum junction and storage temperature range	T _J , T _{Stg}	- 55	-	175	°C	
Thermal registence, junction to cook	R _{thJC}	-	-	0.28		
Thermal resistance, junction to case per module		-	-	0.14	°C/W	
Thermal resistance, case to heatsink	R _{thCS}	-	0.10	-	1	
Weight		-	68	-	g	
Weight		-	2.4	-	oz.	
Mounting torque		35.4 (4)	-	53.1 (6)		
Mounting torque center hole		30 (3.4)	-	40 (4.6)	lbf ⋅ in (N ⋅ m)	
Terminal torque		30 (3.4)	-	44.2 (5)	(14 · 111)	
Vertical pull		-	-	80	llef in	
2" lever pull		-	-	35	lbf ⋅ in	



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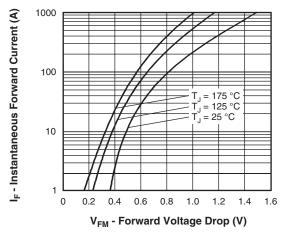


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

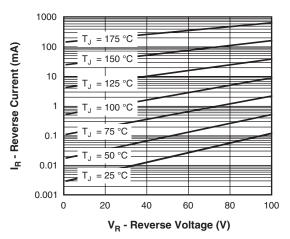


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

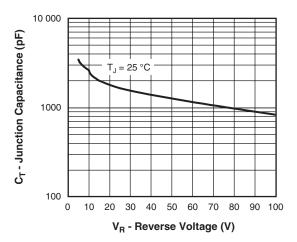


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

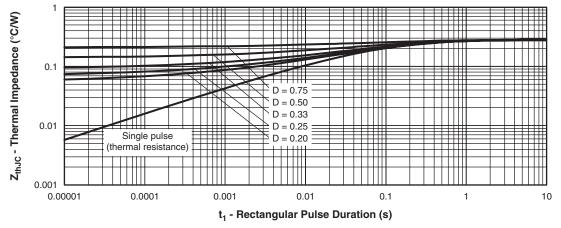


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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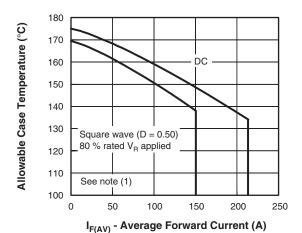


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

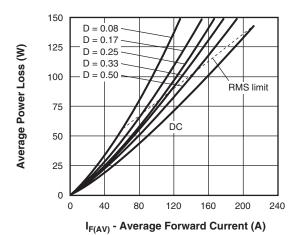


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

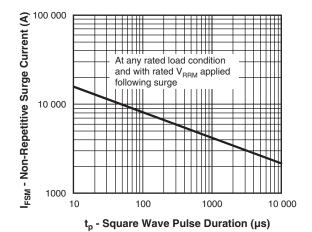


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

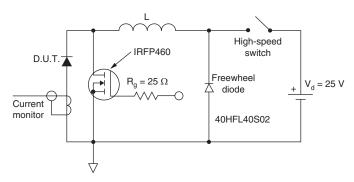


Fig. 8 - Unclamped Inductive Test Circuit

Note

 $^{(1)}$ Formula used: T_C = T_J - (Pd + Pd_{REV}) x R_{th,JC}; Pd = Forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see fig. 6); Pd_{REV} = Inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = 80 % rated V_R

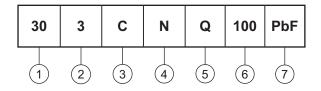
Document Number: 94177 Revision: 28-Apr-08



Schottky Rectifier, 300 A Vishay High Power Products

ORDERING INFORMATION TABLE

Device code



- 1 Average current rating (x 10)
- Product silicon identification
- 3 C = Circuit configuration
- 4 N = Not isolated
- Q = Schottky rectifier diode
- 6 Voltage rating (100 = 100 V)
- 7 Lead (Pb)-free

LINKS TO RELATED DOCUMENTS			
Dimensions	http://www.vishay.com/doc?95021		

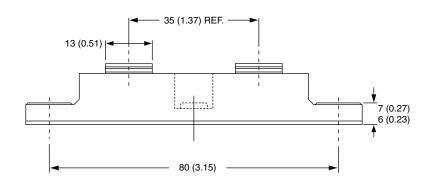
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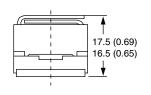


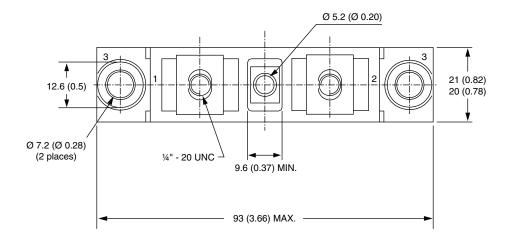
Vishay Semiconductors

TO-244

DIMENSIONS in millimeters (inches)











Vishay

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