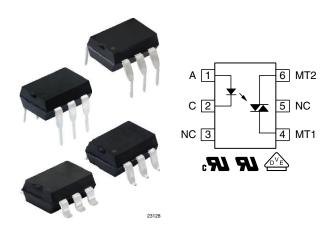
BRT11, BRT12, BRT13

Vishay Semiconductors

Optocoupler, Phototriac Output, Non-Zero Crossing



www.vishay.com

LINKS TO ADDITIONAL RESOURCES



DESCRIPTION

The BRT11, BRT12, and BRT13 product family consists of AC optocouplers non-zero voltage detectors consisting of two electrically insulated lateral power ICs which integrate a thyristor system, a photo detector and noise suppression at the output and an IR GaAs diode input.

FEATURES

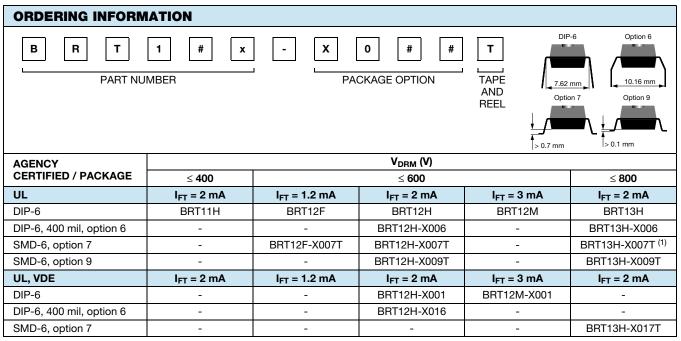
- I_{TRMS} = 300 mA
- High static dV/dt < 10 000 V/µs
- Electrically insulated between input and output circuit
- (e3)
- Microcomputer compatible very low trigger COMPLIANT
 COMPLIANT
- Trigger current
 - (I_{FT} < 1.2 mA) BRT12**F**
 - (I_{FT} < 2 mA) BRT11**H**, BRT12**H**, BRT13**H**
 - (I_{FT} < 3 mA) BRT12**M**, BRT13**M**
- · Non-zero voltage detectors high input sensitivity
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

APPLICATIONS

- Industrial controls
- Office equipment
- Consumer appliances

AGENCY APPROVALS

- <u>UL</u> / <u>cUL</u> 1577
- DIN EN 60747-5-5 (VDE 0884-5), available with option 1



Note

⁽¹⁾ Also available in tube, do not put T on the end

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PARAMETER	TEST CONDITION	PART	SYMBOL	VALUE	UNIT
INPUT					
Reverse voltage			V _R	6	V
Forward current			I _F	20	mA
Surge forward current			I _{FSM}	1.5	А
Power dissipation	t ≤ 10 µs		P _{diss}	30	mW
OUTPUT					
		BRT11	V _{DRM}	400	V
Repetitive peak off-state voltage		BRT12	V _{DRM}	600	V
		BRT13	V _{DRM}	800	V
RMS on-state current			I _{TRMS}	300	mA
Single cycle surge current	50 Hz		I _{TSM}	3	Α
Power dissipation			P _{diss}	600	mW
COUPLER					
Maximum power dissipation			P _{tot}	630	mW
Reference voltage in accordance with VDE 0110 b			V _{ref}	500	V _{RMS}
Reference voltage in accordance with VDE 0110 b (insulation group C)			V _{ref}	600	V _{DC}
Storage temperature range			T _{stg}	-40 to +150	°C
Ambient temperature range			T _{amb}	-40 to +100	°C

Notes

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
maximum ratings for extended periods of the time can adversely affect reliability

 $^{(1)}\,$ Test AC voltage in accordance with DIN 57883, June 1980

PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT	L	L			L	•	
Forward voltage	I _F = 10 mA		V _F	-	1.1	1.35	V
Reverse current	V _R = 6 V		I _R	-	-	10	μA
Thermal resistance, junction to ambient ⁽¹⁾			R _{thJA}	-	-	750	°C/W
OUTPUT	•					•	
Peak off-state voltage	I _{D(RMS)} = 100 μA	BRT11		-	400	-	μA
		BRT12	V _{DM}	-	600	-	μA
		BRT13		-	800	-	μA
Off-state current	$T_{C} = 80 \ ^{\circ}C, \ V_{DRM}$		I _D	-	0.5	100	μA
On-state voltage	I _T = 300 mA		V _T	-	-	2.3	V
Pulse current	$t_p \le 5 \ \mu s$, f = 100 Hz, $dI_{tp}/dt \le 8 \ A/\mu s$		I _{tp}	-	-	2	Α
Critical rate of rise of off-state voltage	$V_D = 0.67 V_{DRM}, T_j = 25 \ ^{\circ}C$		dV/dt _{cr}	10	-	-	kV/µs
	V _D = 0.67 V _{DRM} , T _j = 80 °C		dV/dt _{cr}	5	-	-	kV/μs
Critical rate of rise of voltage at current commutation	V_D = 0.67 V_{DRM} , T_j = 25 °C, dI/dt _{crq} ≤ 15 A/ms		dV/dt _{crq}	10	-	-	kV/µs
	$\label{eq:VD} \begin{split} V_D &= 0.67 \; V_{DRM}, \; T_j = 80 \; ^\circ C, \\ & \text{dI/dt}_{crq} \leq 15 \; \text{A/ms} \end{split}$		dV/dt _{crq}	5	-	-	kV/µs
Critical rate of rise of on-state at current			dl/dt _{cr}	8	-	-	A∕µs
Holding current	V _D = 10 V		Ι _Η	-	80	500	μA
Thermal resistance, junction to ambient			R _{thJA}	-	-	125	°C/W

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ELECTRICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
COUPLER							
Trigger current	$V_D = 10 V, F - versions$		I _{FT}	-	-	1.2	mA
	$V_D = 10 V, H - versions$		I _{FT}	0.4	-	2	mA
	V _D = 10 V, M - versions		I _{FT}	0.8	-	3	mA
Trigger current temperature gradient			$\Delta I_{FT} / \Delta T_j$	-	7	14	µA/°C
Capacitance (input to output)	f = 1 MHz, V _R = 0 V		C _{IO}	-	-	2	pF

Notes

 Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements

⁽¹⁾ Static air, SITAC soldered in PCB or base plate

PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Climatic classification	According to IEC 68 part 1		55 / 100 / 21		
Comparative tracking index		CTI	175		
Maximum rated withstanding isolation voltage	t = 1 min	V _{ISO}	4420	V _{RMS}	
Maximum transient isolation voltage		V _{IOTM}	10 000	V _{peak}	
Maximum repetitive peak isolation voltage		V _{IORM}	890	V _{peak}	
Isolation resistance	$V_{IO} = 500 \text{ V}, \text{ T}_{amb} = 25 ^{\circ}\text{C}$	R _{IO}	≥ 10 ¹²	Ω	
	$V_{IO} = 500 \text{ V}, \text{ T}_{amb} = 100 ^{\circ}\text{C}$	R _{IO}	≥ 10 ¹¹	Ω	
Output safety power		P _{SO}	400	mW	
Input safety current		I _{SI}	275	mA	
Safety temperature		T _S	175	°C	
Creepage distance	DIP-6; SMD-6, option 7;		≥7	mm	
Clearance distance	SMD-6, option 9		≥7	mm	
Creepage distance			≥ 8	mm	
Clearance distance	DIP-6, option 6		≥ 8	mm	
Insulation thickness		DTI	≥ 0.4	mm	

Note

• As per IEC 60747-5-5, § 7.4.3.8.2, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with the safety ratings shall be ensured by means of protective circuits

TYPICAL CHARACTERISTICS (Tamb = 25 °C, unless otherwise specified)

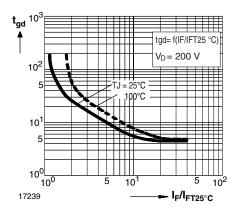


Fig. 1 - Typical Trigger Delay Time

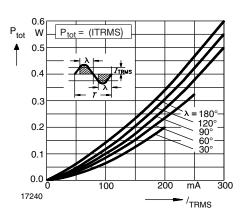


Fig. 2 - Power Dissipation 60 Hz to 60 Hz Line Operation



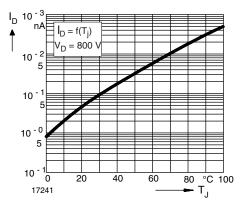


Fig. 3 - Typical Off-State Current

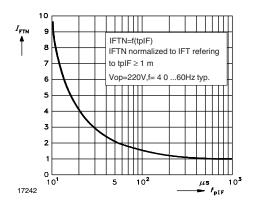


Fig. 4 - Pulse Trigger Current

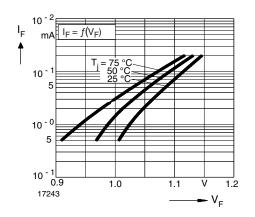


Fig. 5 - Typical Input Characteristics

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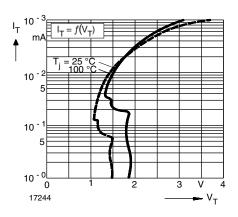


Fig. 6 - Typical Output Characteristics

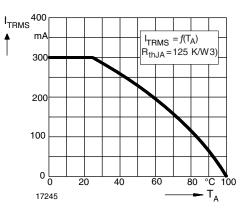


Fig. 7 - Current Reduction

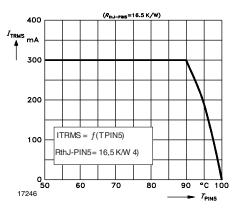


Fig. 8 - Current Reduction

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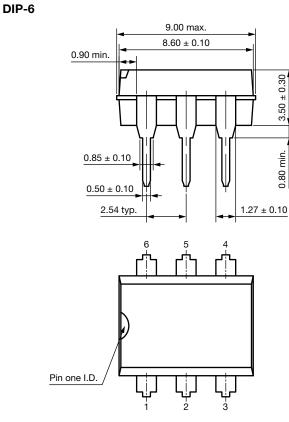
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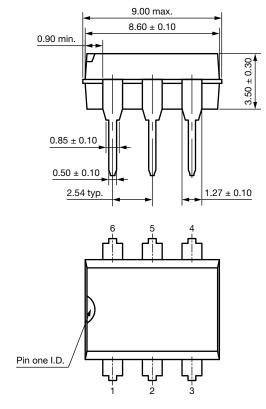
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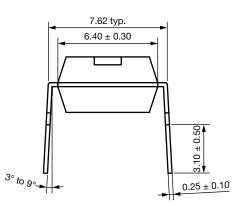
PACKAGE DIMENSIONS in millimeters

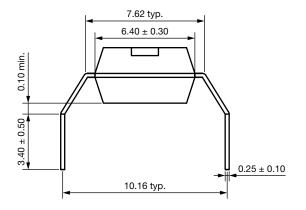


DIP-6, 400 mil (option 6)



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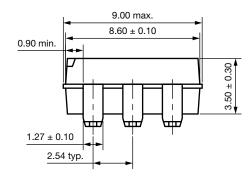
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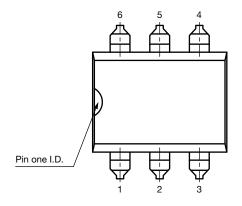
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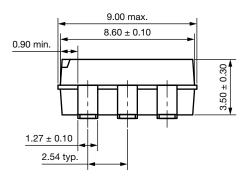


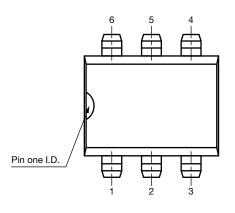
SMD-6 (option 7)





SMD-6 (option 9)

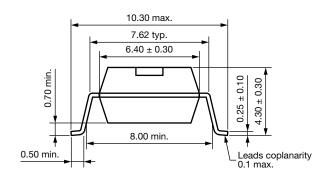


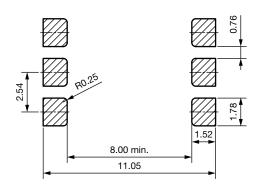


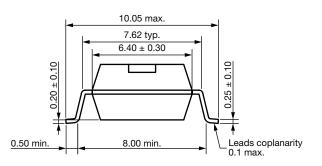
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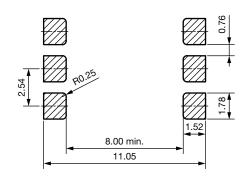
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PACKAGE MARKING (example)



Notes

- "YWW" is the date code marking (Y = year code, WW = week code)
- VDE logo is only marked on option 1 parts
- Tape and reel suffix (T) is not part of the package marking

SOLDER PROFILES

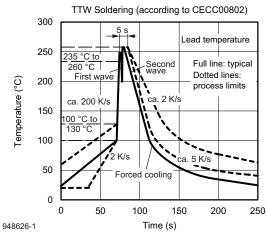


Fig. 9 - Wave Soldering Double Wave Profile According to J-STD-020 for DIP Devices

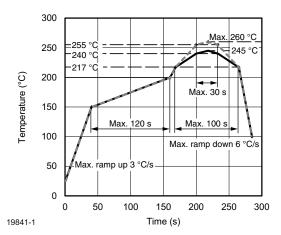


Fig. 10 - Lead (Pb)-free Reflow Solder Profile According to J-STD-020 for SMD Devices

HANDLING AND STORAGE CONDITIONS

ESD level: HBM class 2 Floor life: unlimited Conditions: $T_{amb} < 30$ °C, RH < 85 % Moisture sensitivity level 1, according to J-STD-020



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