



N-Channel 200-V (D-S) 175 °C MOSFET

| PRODUCT SUMMARY | | | | |
|--------------------------|---------------------------------|--------------------|--|--|
| V _{(BR)DSS} (V) | $r_{DS(on)}\left(\Omega\right)$ | I _D (A) | | |
| 200 | 0.030 at V _{GS} = 10 V | 65 ^a | | |

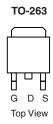
FEATURES

- TrenchFET® Power MOSFET
- 175 °C Junction Temperature
- Low Thermal Resistance Package
- 100 % R_g Tested

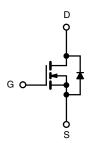
APPLICATIONS

• Isolated DC/DC Converters





Ordering Information: SUM65N20-30-E3 (Lead (Pb)-free)



N-Channel MOSFET

| ABSOLUTE MAXIMUM RATINGS | T _C = 25 °C, unless oth | erwise noted | | |
|---|-------------------------------------|-----------------------------------|------------------|------|
| Parameter | | Symbol | Limit | Unit |
| Drain-Source Voltage | | V _{DS} | 200 | V |
| Gate-Source Voltage | | V _{GS} | ± 20 | V |
| Continuous Drain Current (T _{.1} = 175 °C) | T _C = 25 °C | 1- | 65 ^a | |
| Continuous Diain Current (1) = 175 C) | T _C = 125 °C | I _D | 37 ^a | _ |
| Pulsed Drain Current | | I _{DM} | 140 | _ A |
| Avalanche Current | L = 0.1 mH | I _{AS} | 35 | |
| Single Pulse Avalanche Energy ^b | | E _{AS} | 61 | mJ |
| Mariana Barra Biariantianh | T _C = 25 °C | В | 375 ^c | 14/ |
| Maximum Power Dissipation ^b | T _A = 25 °C ^d | $ P_D$ $-$ | 3.75 | W |
| Operating Junction and Storage Temperature Range | | T _J , T _{stg} | - 55 to 175 | °C |

| THERMAL RESISTANCE RATINGS | | | | | |
|----------------------------|---------------------------------|-------------------|-------|------|--|
| Parameter | | Symbol | Limit | Unit | |
| Junction-to-Ambient | PCB Mount (TO-263) ^d | R_{thJA} | 40 | °C/W | |
| Junction-to-Case (Drain) | | R _{thJC} | 0.4 | C/VV | |

Notes:

- a. Package limited.
- b. Duty cycle ≤ 1 %.
- c. See SOA curve for voltage derating.
- d. When Mounted on 1" square PCB (FR-4 material).

SUM65N20-30

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| Parameter | Symbol | Test Conditions | Min. | Тур. | Max. | Unit | |
|---|----------------------|--|------|-------|-------|------|--|
| Static | | | | | | | |
| Drain-Source Breakdown Voltage | V _{(BR)DSS} | $V_{DS} = 0 \text{ V, } I_{D} = 250 \mu\text{A}$ | 200 | | | V | |
| Gate-Threshold Voltage | V _{GS(th)} | $V_{DS} = V_{GS}, I_D = 250 \mu A$ | 2 | | 4 | V | |
| Gate-Body Leakage | I _{GSS} | $V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$ | | | ± 100 | nA | |
| | | V _{DS} = 200 V, V _{GS} = 0 V | | | 1 | | |
| Zero Gate Voltage Drain Current | I _{DSS} | V _{DS} = 200 V, V _{GS} = 0 V, T _J = 125 °C | | | 50 | μΑ | |
| | | $V_{DS} = 200 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 175 ^{\circ}\text{C}$ | | | 250 | | |
| On-State Drain Current ^a | I _{D(on)} | $V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$ | 120 | | | Α | |
| | | $V_{GS} = 10 \text{ V}, I_D = 30 \text{ A}$ | | 0.023 | 0.030 | Ω | |
| Drain-Source On-State Resistance ^a | r _{DS(on)} | V _{GS} = 10 V, I _D = 30 A, T _J = 125 °C | | | 0.063 | | |
| | | V _{GS} = 10 V, I _D = 30 A, T _J = 175 °C | | | 0.084 | | |
| Forward Transconductance ^a | 9 _{fs} | $V_{DS} = 15 \text{ V}, I_{D} = 30 \text{ A}$ | 25 | | | S | |
| Dynamic ^b | | | | 1 | | | |
| Input Capacitance | C _{iss} | | | 5100 | | | |
| Output Capacitance | C _{oss} | $V_{GS} = 0 \text{ V}, V_{DS} = 25 \text{ V}, f = 1 \text{ MHz}$ | | 480 | | pF | |
| Reverse Transfer Capacitance | C _{rss} | | | 210 | | | |
| Total Gate Charge ^c | Qg | | | 90 | 130 | | |
| Gate-Source Charge ^c | Q_{gs} | $V_{DS} = 100 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 85 \text{ A}$ | | 23 | | nC | |
| Gate-Drain Charge ^c | Q_{gd} | | | 34 | | | |
| Gate Resistance | R_{g} | | 0.5 | 1.7 | 3.3 | Ω | |
| Turn-On Delay Time ^c | t _{d(on)} | | | 24 | 35 | | |
| Rise Time ^c | t _r | V_{DD} = 100 V, R_L = 1.5 Ω | | 220 | 330 | no | |
| Turn-Off Delay Time ^c | t _{d(off)} | $I_D \cong 65 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 2.5 \Omega$ | | 45 | 70 | ns | |
| Fall Time ^c | t _f | | | 200 | 300 | | |
| Source-Drain Diode Ratings and Cha | racteristics 7 | _C = 25 °C ^b | | | | | |
| Continuous Current | Is | | | | 65 | | |
| Pulsed Current | I _{SM} | | 140 | | A | | |
| Forward Voltage ^a | V _{SD} | I _F = 65 A, V _{GS} = 0 V | | 1.0 | 1.5 | V | |
| Reverse Recovery Time | t _{rr} | | | 130 | 200 | ns | |
| Peak Reverse Recovery Current | I _{RM(REC)} | I _F = 50 A, di/dt = 100 A/μs | | 8 | 12 | Α | |
| Reverse Recovery Charge | Q _{rr} | | | 0.52 | 1.2 | μС | |

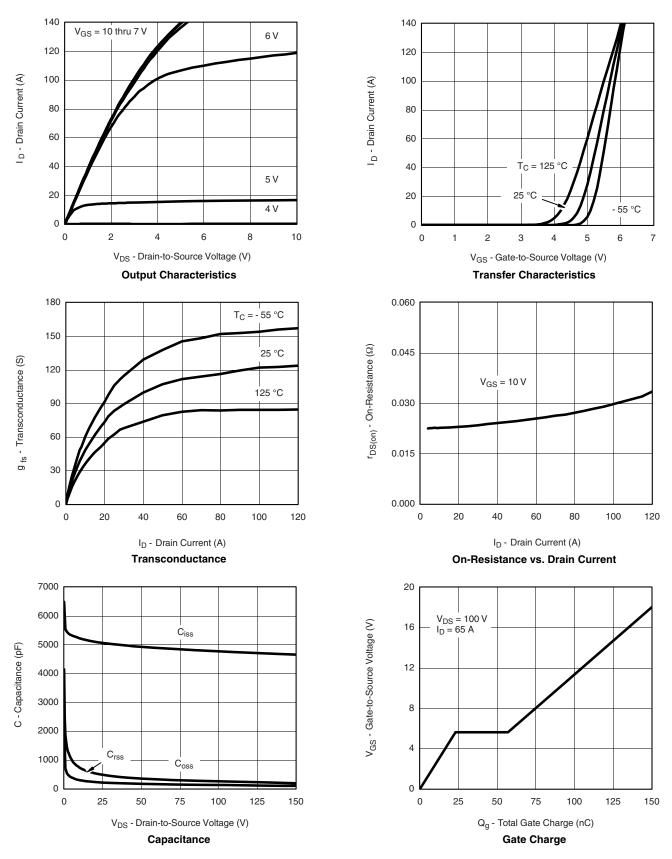
Notes:

- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



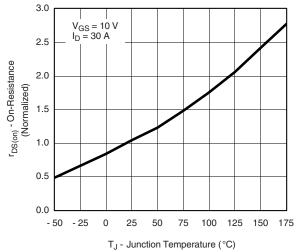
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



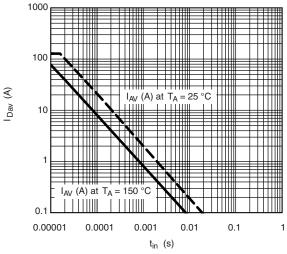
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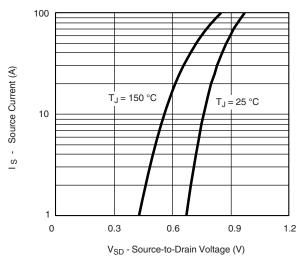
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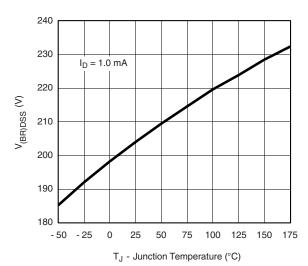
On-Resistance vs. Junction Temperature



Avalanche Current vs. Time



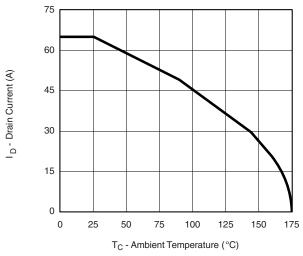
Source-Drain Diode Forward Voltage



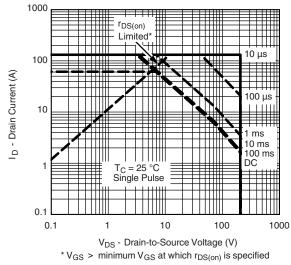
Drain Source Breakdown vs. Junction Temperature



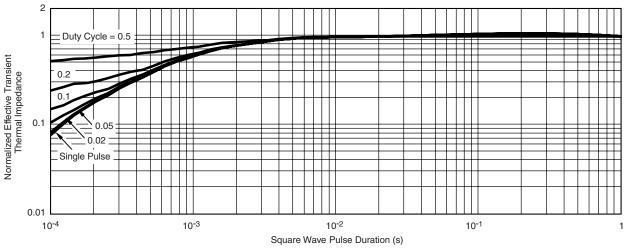
THERMAL RATINGS



Maximum Avalanche and Drain Current vs. Case Temperature



Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Case

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TO-263 (D²PAK): 3-LEAD









DETAIL A (ROTATED 90°)



| _ - b1 | |
|---|--------|
| ≥ | - - |

- 1. Plane B includes maximum features of heat sink tab and plastic.
- 2. No more than 25 % of L1 can fall above seating plane by max. 8 mils.
- 3. Pin-to-pin coplanarity max. 4 mils.
- 4. *: Thin lead is for SUB, SYB. Thick lead is for SUM, SYM, SQM.
- 5. Use inches as the primary measurement.

6. This feature is for thick lead.

| DIM. | | INC | HES | MILLIMETERS | | |
|---------------------------------|------------|-----------|-------|-------------|--------|--|
| | | MIN. | MAX. | MIN. | MAX. | |
| Α | | 0.160 | 0.190 | 4.064 | 4.826 | |
| | b | 0.020 | 0.039 | 0.508 | 0.990 | |
| | b1 | 0.020 | 0.035 | 0.508 | 0.889 | |
| | b2 | 0.045 | 0.055 | 1.143 | 1.397 | |
| c* | Thin lead | 0.013 | 0.018 | 0.330 | 0.457 | |
| C | Thick lead | 0.023 | 0.028 | 0.584 | 0.711 | |
| c1 | Thin lead | 0.013 | 0.017 | 0.330 | 0.431 | |
| CI | Thick lead | 0.023 | 0.027 | 0.584 | 0.685 | |
| | c2 | 0.045 | 0.055 | 1.143 | 1.397 | |
| | D | 0.340 | 0.380 | 8.636 | 9.652 | |
| | D1 | 0.220 | 0.240 | 5.588 | 6.096 | |
| D2 | | 0.038 | 0.042 | 0.965 | 1.067 | |
| D3 | | 0.045 | 0.055 | 1.143 | 1.397 | |
| D4 | | 0.044 | 0.052 | 1.118 | 1.321 | |
| E | | 0.380 | 0.410 | 9.652 | 10.414 | |
| | E1 | 0.245 | - | 6.223 | = | |
| | E2 | 0.355 | 0.375 | 9.017 | 9.525 | |
| | E3 | 0.072 | 0.078 | 1.829 | 1.981 | |
| е | | 0.100 |) BSC | 2.54 BSC | | |
| K | | 0.045 | 0.055 | 1.143 | 1.397 | |
| L | | 0.575 | 0.625 | 14.605 | 15.875 | |
| L1 | | 0.090 | 0.110 | 2.286 | 2.794 | |
| L2 | | 0.040 | 0.055 | 1.016 | 1.397 | |
| L3 | | 0.050 | 0.070 | 1.270 | 1.778 | |
| | L4 | 0.010 BSC | | 0.254 BSC | | |
| | М | - | 0.002 | - | 0.050 | |
| ECN: T13-0707-Rev. K, 30-Sep-13 | | | | | | |

DWG: 5843





RECOMMENDED MINIMUM PADS FOR D²PAK: 3-Lead



Recommended Minimum Pads Dimensions in Inches/(mm)

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