**WINSTAR**

# WINSTAR Display Co.,Ltd.

# 華凌光電股份有限公司

# SPECIFICATION

**CUSTOMER :**

**MODULE NO.:  WO12864C2-TFH#**

| APPROVED BY: | | |
|---|---|---|
| ( FOR CUSTOMER USE ONLY ) | PCB VERSION: | DATA: |

| SALES BY | APPROVED BY | CHECKED BY | PREPARED BY |
|---|---|---|---|
| | | | |

| VERSION | DATE | REVISED PAGE NO. | SUMMARY |
|---|---|---|---|
| K | 2019/12/17 | | Modify Precautions in use of LCD Modules |

**Winstar Display Co.,**

華凌光電股份有限公司

| MODLE NO： | |
|---|---|

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<tr><td>0</td><td>2009/01/15</td><td></td><td>First issue</td></tr>
<tr><td>A</td><td>2009/02/20</td><td></td><td>Correct IC=ST7565P</td></tr>
<tr><td>B</td><td>2011/05/03</td><td></td><td>Modify V0-VSS</td></tr>
<tr><td>C</td><td>2012/07/17</td><td></td><td>Modify Backlight Information</td></tr>
<tr><td>D</td><td>2013/11/21</td><td></td><td>Remove IC information Add Pull Tape</td></tr>
<tr><td>E</td><td>2014/09/30</td><td></td><td>Correct Response Time.</td></tr>
<tr><td>F</td><td>2016/01/27</td><td></td><td>Modify Precautions in use of LCD Modules & Static electricity test</td></tr>
<tr><td>G</td><td>2016/11/18</td><td></td><td>Add FPC bending rule</td></tr>
<tr><td>H</td><td>2018/12/03</td><td></td><td>Modify Luminance.</td></tr>
<tr><td>I</td><td>2019/07/23</td><td></td><td>Correct Interface Pin Function.</td></tr>
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# Contents

# 1.Module Classification Information

<u>W</u>    <u>O</u>    <u>12864</u>    <u>C2</u>   ─   <u>T</u>   <u>F</u>   <u>H</u>   ─    <u>#</u>

①     ②     ③     ④       ⑤   ⑥   ⑦       ⑧

①   Brand：WINSTAR DISPLAY CORPORATION

②   Display Type：H→Character Type, G→Graphic Type , X→TAB Type, O→COG Type

③   Display Font：   128 * 64 dot

④   Model serials no.

⑤   Backlight Type：

| | | |
|---|---|---|
| N→Without backlight | T→LED, White | L→LED, Full color |
| B→EL, Blue green | A→LED, Amber | J→DIP LED,Blue |
| D→EL, Green | R→LED, Red | K→DIP LED,White |
| W→EL, White | O→LED, Orange | E→DIP LED, Yellow Green |
| M→EL, Yellow Green | G→LED, Green | H→DIP LED,Amber |
| F→CCFL, White | P→LED, Blue | I→DIP LED, Red |
| Y→LED, Yellow Green | X→LED, Dual color | |
| G→LED, Green | C→LED, Full color | |

⑥   LCD Mode：

| | |
|---|---|
| B→TN Positive, Gray | V→FSTN Negative, Blue |
| N→TN Negative, | T→FSTN Negative, Black |
| L→VA Negative | D→FSTN Negative (Double film) |
| H→ HTN Positive, Gray | F→FSTN Positive |
| I→HTN Negative, Black | K→FSC Negative |
| U→HTN Negative, Blue | S→FSC Positive |
| M→STN Negative, Blue | E→ISTN Negative, Black |
| G→STN Positive, Gray | C→CSTN Negative, Black |
| Y→STN Positive, Yellow Green | A→ASTN Negative, Black |

⑦   LCD Polarize Type/ Temperature range/ View direction

| | |
|---|---|
| A→Reflective, N.T, 6:00 | H→Transflective, W.T,6:00 |
| D→Reflective, N.T, 12:00 | K→Transflective, W.T,12:00 |
| G→Reflective, W. T, 6:00 | C→Transmissive, N.T,6:00 |
| J→Reflective, W. T, 12:00 | F→Transmissive, N.T,12:00 |
| B→Transflective, N.T,6:00 | I→Transmissive, W. T, 6:00 |
| E→Transflective, N.T.12:00 | L→Transmissive, W.T,12:00 |

⑧   Special Code      #:Fit in with the ROHS Directions and regulations

# 2.Precautions in use of LCD Modules

(1)Avoid applying excessive shocks to the module or making any alterations or modifications to it.

(2)Don't make extra holes on the printed circuit board, modify its shape or change the components of LCD module.

(3)Don't disassemble the LCM.

(4)Don't operate it above the absolute maximum rating.

(5)Don't drop, bend or twist LCM.

(6)Soldering: only to the I/O terminals.

(7)Storage: please storage in anti-static electricity container and clean environment.

(8) Winstar have the right to change the passive components, including R3,R6 & backlight adjust resistors. (Resistors,capacitors and other passive components will have different appearance and color caused by the different supplier.)

(9)Winstar have the right to change the PCB Rev. (In order to satisfy the supplying stability, management optimization and the best product performance...etc, under the premise of not affecting the electrical characteristics and external dimensions, Winstar have the right to modify the version.)

(10) To ensure the stability of the display screen, please apply screen saver after showing 30 mins of fixed display content.

(11) The limitation of FPC bending

Display area

Min 2mm

R = 1.5mm
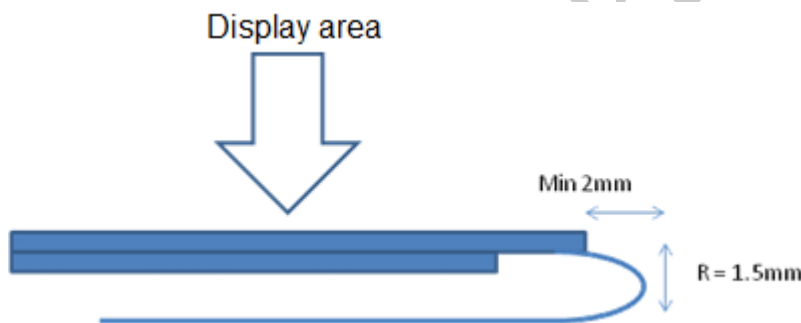
(12)Please heat up a little the tape sticking on the components when removing it; otherwise the components might be damaged.

# 3.General Specification

| Item | Dimension | Unit |
|---|---|---|
| Number of Characters | 128 x 64 dots | ─ |
| Module dimension | 55.2x 39.8 x 6.5(MAX) | mm |
| View area | 45.2 x 27.0 | mm |
| Active area | 40.92 x 24.28 | mm |
| Dot size | 0.28 x 0.34 | mm |
| Dot pitch | 0.32 x 0.38 | mm |
| LCD type | FSTN Positive Transflective<br>(In LCD production, It will occur slightly color difference. We can only guarantee the same color in the same batch.) | |
| Duty | 1/64 , 1/9 Bias | |
| View direction | 6 o'clock | |
| Backlight Type | LED White | |
| IC | ST7565P | |

# 4.Absolute Maximum Ratings

| Item | Symbol | Min | Typ | Max | Unit |
|---|---|---|---|---|---|
| Operating Temperature | $T_{OP}$ | -20 | — | +70 | ℃ |
| Storage Temperature | $T_{ST}$ | -30 | — | +80 | ℃ |
| Power Supply Voltage | VDD | -0.3 | — | 3.6 | V |
| Power supply voltage (VDD standard) | V0, VOUT | -0.3 | — | 14.5 | V |
| Power supply voltage (VDD standard) | V1, V2, V3, V4 | -0.3 | — | V0+0.3 | V |

# 5.Electrical Characteristics

| Item | Symbol | Condition | Min | Typ | Max | Unit |
|---|---|---|---|---|---|---|
| Supply Voltage For Logic | $V_{DD}$-$V_{SS}$ | — | 2.7 | 3.0 | 3.3 | V |
| Supply Voltage For LCM | VOP | Ta=-20℃ | — | — | — | V |
| | | Ta=25℃ | 9.4 | 9.6 | 9.8 | V |
| | | Ta=70℃ | — | — | — | V |
| Input High Volt. | $V_{IH}$ | — | 0.8 $V_{DD}$ | — | $V_{DD}$ | V |
| Input Low Volt. | $V_{IL}$ | — | Vss | — | 0.2 $V_{DD}$ | V |
| Output High Volt. | $V_{OH}$ | — | 0.8 $V_{DD}$ | — | $V_{DD}$ | V |
| Output Low Volt. | $V_{OL}$ | — | Vss | — | 0.2$V_{DD}$ | V |
| Supply Current(No include LED Backlight) | $I_{DD}$ | $V_{DD}$=3.0V | — | 0.49 | 1.0 | mA |

NOTE 1: Please kindly consider to design the Vop to be adjustable while programing the software to match LCD contrast tolerance
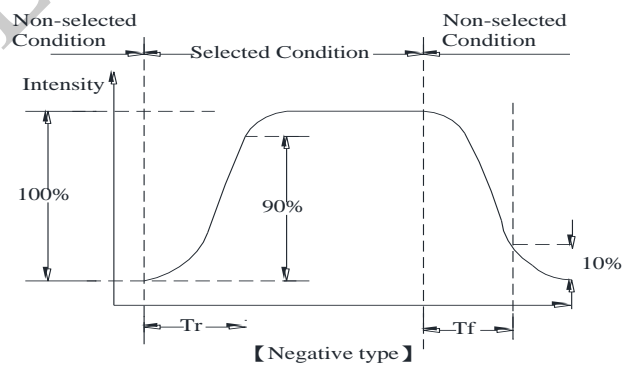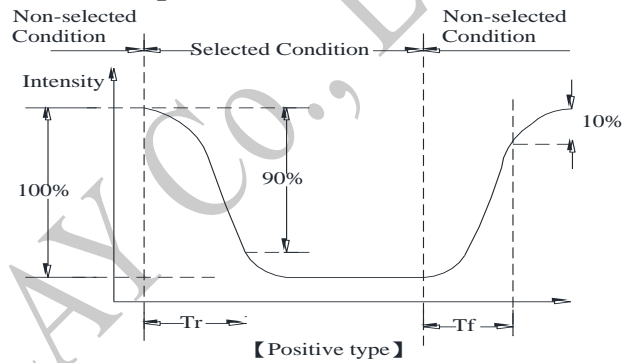
# 6.Optical Characteristics

| Item | Symbol | Condition | Min | Typ | Max | Unit |
|------|--------|-----------|-----|-----|-----|------|
| View Angle | $\theta$ | $CR \geqq 2$ | 0 | — | 30 | $\phi = 180°$ |
| | $\theta$ | $CR \geqq 2$ | 0 | — | 60 | $\phi = 0°$ |
| | $\theta$ | $CR \geqq 2$ | 0 | — | 45 | $\phi = 90°$ |
| | $\theta$ | $CR \geqq 2$ | 0 | — | 45 | $\phi = 270°$ |
| Contrast Ratio | CR | — | — | 5 | — | — |
| Response Time | T rise | — | — | 200 | 300 | ms |
| | T fall | — | — | 250 | 350 | ms |

**Definition of Operation Voltage (Vop)**
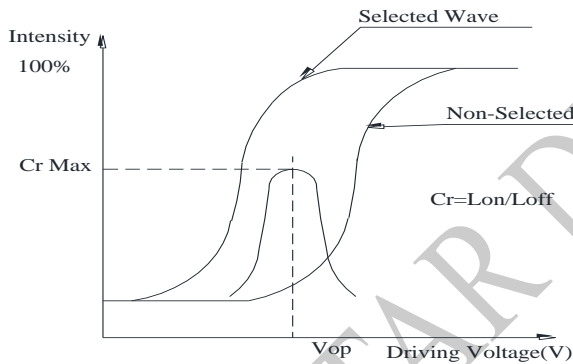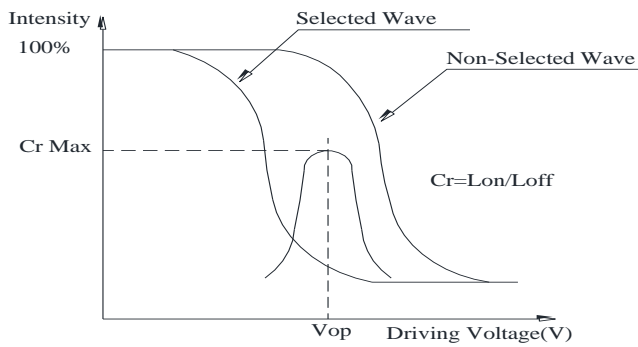
**Definition of Response Time ( Tr , Tf )**

Intensity
100%
Selected Wave
Non-Selected Wave
Cr Max
Cr=Lon/Loff
Vop    Driving Voltage(V)

Non-selected Condition — Selected Condition — Non-selected Condition
Intensity
100%    90%    10%
Tr    Tf
【 Positive type 】

Intensity
100%
Selected Wave
Non-Selected Wave
Cr Max
Cr=Lon/Loff
Vop    Driving Voltage(V)

Non-selected Condition — Selected Condition — Non-selected Condition
Intensity
100%    90%    10%
Tr    Tf
【 Negative type 】

**Conditions :**

Operating Voltage : Vop    Viewing Angle( $\theta$ ， $\varphi$ ) : 0°， 0°
Frame Frequency : 64 HZ    Driving Waveform : 1/N duty , 1/a bias

**Definition of viewing angle(CR$\geqq$2)**

$\theta f$    $\theta b$    $\varphi = 180°$
$\theta 1$    $\theta r$
$\varphi = 270°$    $\varphi = 90°$
$\varphi = 0°$
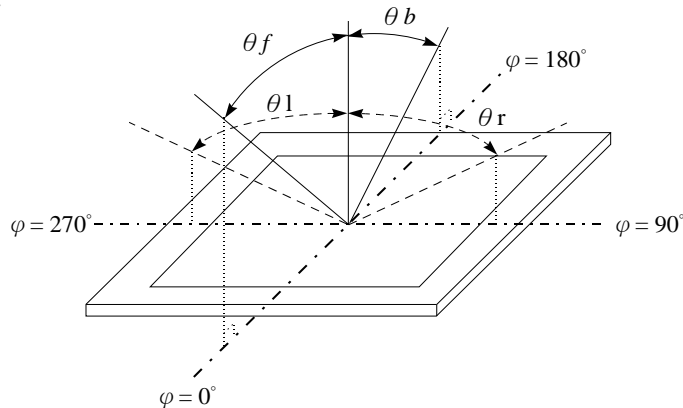
# 7.Interface Pin Function

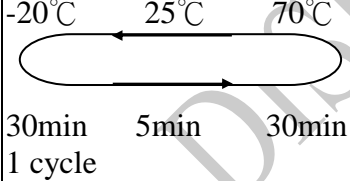| Pin No. | Symbol | Level | Description |
|---------|--------|-------|-------------|
| 1 | IRS | I | This terminal selects the resistors for the V0 voltage level adjustment.<br>IRS = "H": Use the internal resistors<br>IRS = "L": Do not use the internal resistors. The V0 voltage level is regulated by an external resistive voltage divider attached to the VR terminal |
| 2 | /HPM | I | This is the power control terminal for the power supply circuit for liquid crystal drive.<br>/HPM = "H": Normal mode<br>/HPM = "L": High power mode (Default) |
| 3 | P/S | I | This is the parallel data input/serial data input switch terminal.<br>P/S = "H": Parallel data input.<br>P/S = "L": Serial data input.<br>The following applies depending on the P/S status:<br><br>| P/S | Data/Command | Data | Read/Write | Serial Clock |<br>|-----|--------------|------|------------|--------------|<br>| "H" | A0 | D0 to D7 | /RD, /WR | X |<br>| "L" | A0 | SI (D7) | Write only | SCL (D6) |<br><br>When P/S = "L", D0 to D5 fixed    "H".<br>/RD (E) and /WR (R/W) are fixed to either "H" or "L".<br>With serial data input, It is impossible read data from RAM |
| 4 | C86 | I | This is the MPU interface selection pin.<br>C86 = "H": 6800 Series MPU interface.<br>C86 = "L": 8080 Series MPU interface |
| 5 | VR | I | Output voltage regulator terminal. Provides the voltage between VSS and V0 through a resistive voltage divider.<br>IRS = "L" : the V0 voltage regulator internal resistors are not used.<br>IRS = "H" : the V0 voltage regulator internal resistors are used. |
| 6~10 | V0~V4 | Power Supply | This is a multi-level power supply for the liquid crystal drive. |
| 11 | VRS | Power Supply | This is the internal-output VREG power supply for the LCD power supply voltage regulator. |
| 12 | CAP4+ | O | DC/DC voltage converter. |
| 13 | CAP2- | O | DC/DC voltage converter. Connect a capacitor between this terminal and the CAP2P terminal. |

| 14 | CAP2+ | O | DC/DC voltage converter. Connect a capacitor between this terminal and the CAP2N terminal. |
|---|---|---|---|
| 15 | CAP1+ | O | DC/DC voltage converter. Connect a capacitor between this terminal and the CAP1N terminal. |
| 16 | CAP1- | O | DC/DC voltage converter. Connect a capacitor between this terminal and the CAP1P terminal. |
| 17 | CAP3+ | O | DC/DC voltage converter. Connect a capacitor between this terminal and the CAP1N terminal. |
| 18 | CAP5+ | O | DC/DC voltage converter. |
| 19 | VOUT | O | DC/DC voltage converter. Connect a capacitor between this terminal and vss or VDD |
| 20 | VSS | Power Supply | Ground |
| 21 | VDD | Power Supply | Power supply |
| 22~29 | D7~D0 | I/O | Data bus line |
| 30 | /RD(E) | I | • When connected to 8080 series MPU, this pin is treated as the "/RD" signal of the 8080 MPU and is LOW-active.<br>The data bus is in an output status when this signal is "L".<br>• When connected to 6800 series MPU, this pin is treated as the "E" signal of the 6800 MPU and is HIGH-active.<br>This is the enable clock input terminal of the 6800 Series MPU. |
| 31 | /WR(R/W) | I | • When connected to 8080 series MPU, this pin is treated as the "/WR" signal of the 8080 MPU and is LOW-active.<br>The signals on the data bus are latched at the rising edge of the /WR signal.<br>• When connected to 6800 series MPU, this pin is treated as the "R/W" signal of the 6800 MPU and decides the access type :<br>When R/W = "H": Read.<br>When R/W = "L": Write. |
| 32 | A0 | I | This is connect to the least significant bit of the normal MPU address bus, and it determines whether the data bits are data or command.<br>A0 = "H": Indicates that D0 to D7 are display data.<br>A0 = "L": Indicates that D0 to D7 are control data. |
| 33 | /RES | I | When RES is set to "L", the setting are initialized. |
| 34 | /CS1 | I | This is the chip select signal. |

# 8.Contour Drawing

| PIN NO. | SIGNAL | PIN NO. | SIGNAL |
|---------|--------|---------|--------|
| 1 | IRS | 18 | CAP5+ |
| 2 | /HPM | 19 | VOUT |
| 3 | P/S | 20 | Vss |
| 4 | C86 | 21 | VDD |
| 5 | VR | 22 | D7 |
| 6 | V0 | 23 | D6 |
| 7 | V1 | 24 | D5 |
| 8 | V2 | 25 | D4 |
| 9 | V3 | 26 | D3 |
| 10 | V4 | 27 | D2 |
| 11 | VRS | 28 | D1 |
| 12 | CAP4+ | 29 | D0 |
| 13 | CAP2- | 30 | /RD(E) |
| 14 | CAP2+ | 31 | /WR(R/W) |
| 15 | CAP1+ | 32 | A0 |
| 16 | CAP1- | 33 | /RES |
| 17 | CAP3+ | 34 | /CS1 |

128x64 DOTS

DOT SIZE
SCALE 20/1

0.32
0.28
0.38
0.34

Contact Side
SCALE 3/2

W=0.25
16.5
(P0.5*33)
17.5
0.5

1.0
4-ø1.0
53.2
55.2±0.5
53.2(LCD)
45.2(VA)
40.92(AA)
5.0
7.14
1.0
3.3
4.66
39.8±0.5
37.8(LCD)
27.0(VA)
24.28(AA)
15.6
15.2
79.0±1.0
20.0
2.0
(18.85)
17.5
8.0±1.0
Pull Tape

6.0±0.5
5.0
3.20
35.4
0.6
1.25
2.4
0.3±0.05

Supporting tape   Contact side

The non-specified tolerance of dimension is ±0.3mm.

3.0
49.2
3.50
K
A
W=1.2
5.3
44.6

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# 9.Reliability

**Content of Reliability Test (Wide temperature, -20℃~70℃)**

<table>
<tr><td colspan="4" align="center"><b>Environmental Test</b></td></tr>
<tr><td align="center"><b>Test Item</b></td><td align="center"><b>Content of Test</b></td><td align="center"><b>Test Condition</b></td><td align="center"><b>Note</b></td></tr>
<tr><td>High Temperature storage</td><td>Endurance test applying the high storage temperature for a long time.</td><td>80℃<br>200hrs</td><td>2</td></tr>
<tr><td>Low Temperature storage</td><td>Endurance test applying the low storage temperature for a long time.</td><td>-30℃<br>200hrs</td><td>1,2</td></tr>
<tr><td>High Temperature Operation</td><td>Endurance test applying the electric stress (Voltage & Current) and the thermal stress to the element for a long time.</td><td>70℃<br>200hrs</td><td>—<br>—</td></tr>
<tr><td>Low Temperature Operation</td><td>Endurance test applying the electric stress under low temperature for a long time.</td><td>-20℃<br>200hrs</td><td>1</td></tr>
<tr><td>High Temperature/ Humidity storage</td><td>The module should be allowed to stand at 60℃,90%RH max<br>For 96hrs under no-load condition excluding the polarizer, Then taking it out and drying it at normal temperature.</td><td>60℃,90%RH<br>96hrs</td><td>1,2</td></tr>
<tr><td>Thermal shock resistance</td><td>The sample should be allowed stand the following 10 cycles of operation<br>-20℃    25℃    70℃<br><br>30min   5min   30min<br>1 cycle</td><td>-20℃/70℃<br>10 cycles</td><td>—<br>—</td></tr>
<tr><td>Vibration test</td><td>Endurance test applying the vibration during transportation and using.</td><td>Total fixed amplitude : 1.5mm<br>Vibration Frequency : 10~55Hz<br>One cycle 60 seconds to 3 directions of X,Y,Z for<br>Each 15 minutes</td><td>3</td></tr>
<tr><td>Static electricity test</td><td>Endurance test applying the electric stress to the terminal.</td><td>VS=±600V(contact), ±800v(air),<br>RS=330Ω<br>CS=150pF<br>10 times</td><td>—<br>—</td></tr>
</table>

**Note1: No dew condensation to be observed.**

**Note2: The function test shall be conducted after 4 hours storage at the normal**

**Temperature and humidity after remove from the test chamber.**

**Note3: The packing have to including into the vibration testing.**

# 10.Backlight Information

## Specification

| PARAMETER | SYMBOL | MIN | TYP | MAX | UNIT | TEST    CONDITION |
|-----------|--------|-----|-----|-----|------|-------------------|
| Supply Current | ILED | 36 | 48 | 60 | mA | V=3.5V |
| Supply Voltage | V | 3.4 | 3.5 | 3.6 | V | |
| Reverse Voltage | VR | — | — | 5 | V | — |
| Luminance (Without LCD) | IV | 800 | 1000 | — | CD/M$^2$ | ILED=48mA |
| LED Life Time (For Reference only) | — | — | 50000 | — | Hr. | I$_{LED}\leqq$48mA 25℃,50-60%RH, (Note 1) |
| Color | White | | | | | |

Note: The LED of B/L is drive by current only；driving voltage is only for reference

   To make driving current in safety area (waste current between minimum and

   maximum).
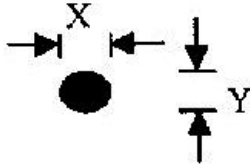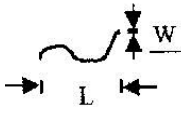
Note 1:50K hours is only an estimate for reference.

LED B\L Drive Method
1.Drive from A , K
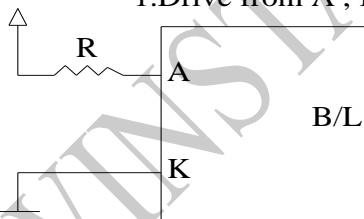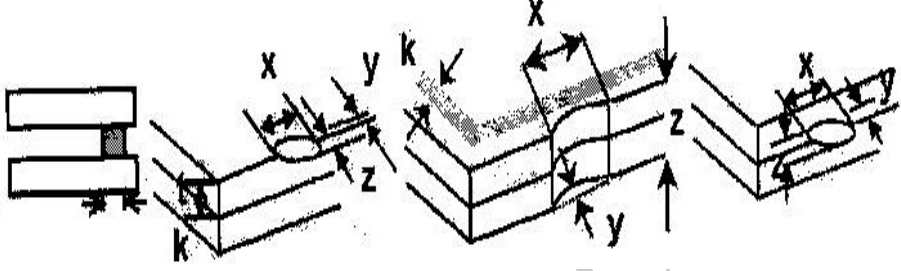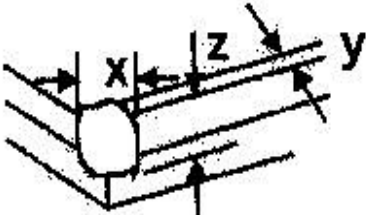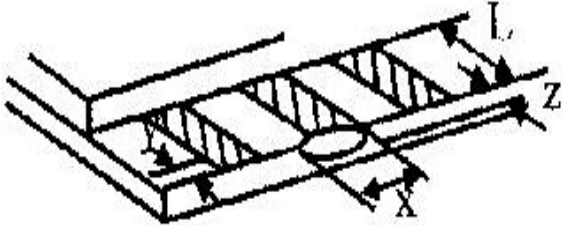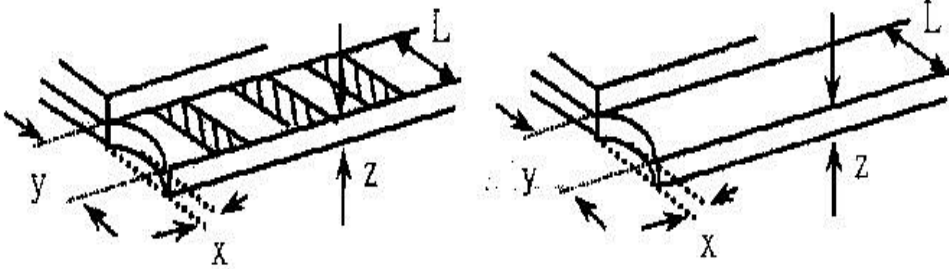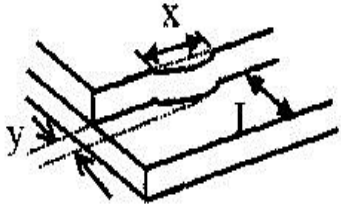
# 11.Inspection specification
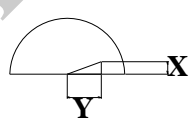
| NO | Item | Criterion | AQL |
|---|---|---|---|
| 01 | Electrical Testing | Missing vertical, horizontal segment, segment contrast defect.<br>Missing character , dot or icon.<br>Display malfunction.<br>No function or no display.<br>Current consumption exceeds product specifications.<br>LCD viewing angle defect.<br>Mixed product types.<br>Contrast defect. | 0.65 |
| 02 | Black or white spots on LCD (display only) | 2.1 White and black spots on display ≦0.25mm, no more than three white or black spots present.<br>2.2 Densely spaced: No more than two spots or lines within 3mm | 2.5 |
| 03 | LCD black spots, white spots, contamination (non-display) | 3.1 Round type : As following drawing<br>Φ=( x + y ) / 2<br><br>| SIZE | Acceptable Q TY |<br>|---|---|<br>| Φ≦0.10 | Accept no dense |<br>| 0.10＜Φ≦0.20 | 2 |<br>| 0.20＜Φ≦0.25 | 1 |<br>| 0.25＜Φ | 0 | | 2.5 |
| | | 3.2 Line type : (As following drawing)<br><br>| Length | Width | Acceptable Q TY |<br>|---|---|---|<br>| --- | W≦0.02 | Accept no dense |<br>| L≦3.0 | 0.02＜W≦0.03 | 2 |<br>| L≦2.5 | 0.03＜W≦0.05 | |<br>| --- | 0.05＜W | As round type | | 2.5 |

| 04 | Polarizer bubbles | If bubbles are visible, judge using black spot specifications, not easy to find, must check in specify direction. | Size Φ | Acceptable Q TY | 2.5 |
|---|---|---|---|---|---|
| | | | $\Phi \leqq 0.20$ | Accept no dense | |
| | | | $0.20 < \Phi \leqq 0.50$ | 3 | |
| | | | $0.50 < \Phi \leqq 1.00$ | 2 | |
| | | | $1.00 < \Phi$ | 0 | |
| | | | Total Q TY | 3 | |

| NO | Item | Criterion | AQL |
|----|------|-----------|-----|
| 05 | Scratches | Follow NO.3 LCD black spots, white spots, contamination | |
| 06 | Chipped glass | Symbols Define:<br><br>x: Chip length      y: Chip width      z: Chip thickness<br>k: Seal width      t: Glass thickness      a: LCD side length<br>L: Electrode pad length:<br><br>6.1 General glass chip :<br>6.1.1 Chip on panel surface and crack between panels:<br><br><br><br>| z: Chip thickness | y: Chip width | x: Chip length |<br>|---|---|---|<br>| $Z \leqq 1/2t$ | Not over viewing area | $x \leqq 1/8a$ |<br>| $1/2t < z \leqq 2t$ | Not exceed 1/3k | $x \leqq 1/8a$ |<br><br>⊙If there are 2 or more chips, x is total length of each chip.<br><br>6.1.2 Corner crack:<br><br><br><br>| z: Chip thickness | y: Chip width | x: Chip length |<br>|---|---|---|<br>| $Z \leqq 1/2t$ | Not over viewing area | $x \leqq 1/8a$ |<br>| $1/2t < z \leqq 2t$ | Not exceed 1/3k | $x \leqq 1/8a$ |<br><br>⊙If there are 2 or more chips, x is the total length of each chip. | 2.5 |

| NO | Item | Criterion | AQL |
|---|---|---|---|
| 06 | Glass crack | Symbols :<br>x: Chip length　　　y: Chip width　　　z: Chip thickness<br>k: Seal width　　　t: Glass thickness　a: LCD side length<br>L: Electrode pad length<br>6.2 Protrusion over terminal :<br>6.2.1 Chip on electrode pad :<br><br><br><br>6.2.2 Non-conductive portion:<br><br><br><br>⊙If the chipped area touches the ITO terminal, over 2/3 of the ITO must remain and be inspected according to electrode terminal specifications.<br>⊙If the product will be heat sealed by the customer, the alignment mark not be damaged.<br>6.2.3 Substrate protuberance and internal crack.<br><br> | 2.5 |

6.2.1 Chip on electrode pad :

| y: Chip width | x: Chip length | z: Chip thickness |
|---|---|---|
| $y \leqq 0.5mm$ | $x \leqq 1/8a$ | $0 < z \leqq t$ |

6.2.2 Non-conductive portion:

| y: Chip width | x: Chip length | z: Chip thickness |
|---|---|---|
| $y \leqq L$ | $x \leqq 1/8a$ | $0 < z \leqq t$ |

6.2.3 Substrate protuberance and internal crack.

| y: width | x: length |
|---|---|
| $y \leqq 1/3L$ | $x \leqq a$ |

| NO | Item | Criterion | AQL |
|----|------|-----------|-----|
| 07 | Cracked glass | The LCD with extensive crack is not acceptable. | 2.5 |
| 08 | Backlight elements | 8.1 Illumination source flickers when lit. | 0.65 |
| | | 8.2 Spots or scratched that appear when lit must be judged. Using LCD spot, lines and contamination standards. | 2.5 |
| | | 8.3 Backlight doesn't light or color wrong. | 0.65 |
| 09 | Bezel | 9.1 Bezel may not have rust, be deformed or have fingerprints, stains or other contamination. | 2.5 |
| | | 9.2 Bezel must comply with job specifications. | 0.65 |
| 10 | PCB、COB | 10.1 COB seal may not have pinholes larger than 0.2mm or contamination. | 2.5 |
| | | 10.2 COB seal surface may not have pinholes through to the IC. | 2.5 |
| | | 10.3 The height of the COB should not exceed the height indicated in the assembly diagram. | 0.65 |
| | | 10.4 There may not be more than 2mm of sealant outside the seal area on the PCB. And there should be no more than three places. | 2.5 |
| | | 10.5 No oxidation or contamination PCB terminals. | 2.5 |
| | | 10.6 Parts on PCB must be the same as on the production characteristic chart. There should be no wrong parts, missing parts or excess parts. | 0.65 |
| | | 10.7 The jumper on the PCB should conform to the product characteristic chart. | 0.65 |
| | | 10.8 If solder gets on bezel tab pads, LED pad, zebra pad or screw hold pad, make sure it is smoothed down. | 2.5 |
| | | 10.9 The Scraping testing standard for Copper Coating of PCB<br><br>$X * Y <= 2mm^2$ | 2.5 |
| 11 | Soldering | 11.1 No un-melted solder paste may be present on the PCB. | 2.5 |
| | | 11.2 No cold solder joints, missing solder connections, oxidation or icicle. | 2.5 |
| | | 11.3 No residue or solder balls on PCB. | 2.5 |
| | | 11.4 No short circuits in components on PCB. | 0.65 |

| NO | Item | Criterion | AQL |
|---|---|---|---|
| 12 | General appearance | 12.1 No oxidation, contamination, curves or, bends on interface Pin (OLB) of TCP. | 2.5 |
| | | 12.2 No cracks on interface pin (OLB) of TCP. | 0.65 |
| | | 12.3 No contamination, solder residue or solder balls on product. | 2.5 |
| | | 12.4 The IC on the TCP may not be damaged, circuits. | 2.5 |
| | | 12.5 The uppermost edge of the protective strip on the interface pin must be present or look as if it cause the interface pin to sever. | 2.5 |
| | | 12.6 The residual rosin or tin oil of soldering (component or chip component) is not burned into brown or black color. | 2.5 |
| | | 12.7 Sealant on top of the ITO circuit has not hardened. | 2.5 |
| | | 12.8 Pin type must match type in specification sheet. | 0.65 |
| | | 12.9 LCD pin loose or missing pins. | 0.65 |
| | | 12.10 Product packaging must the same as specified on packaging specification sheet. | 0.65 |
| | | 12.11 Product dimension and structure must conform to product specification sheet. | 0.65 |
| | | 12.12 Visual defect outside of VA is not considered to be rejection. | 0.65 |

# 12.Material List of Components for RoHs

1. WINSTAR Display Co., Ltd hereby declares that all of or part of products (with the mark "#"in code), including, but not limited to, the LCM, accessories or packages, manufactured and/or delivered to your company (including your subsidiaries and affiliated company) directly or indirectly by our company (including our subsidiaries or affiliated companies) do not intentionally contain any of the substances listed in all applicable EU directives and regulations, including the following substances.

Exhibit A：The Harmful Material List

| Material | Cd | Pb | Hg | Cr6+ | PBB | PBDE | DEHP | BBP | DBP | DIBP |
|---|---|---|---|---|---|---|---|---|---|---|
| Limited Value | 100 ppm | 1000 ppm | 1000 ppm | 1000 ppm | 1000 ppm | 1000 ppm | 1000 ppm | 1000 ppm | 1000 ppm | 1000 ppm |
| Above limited value is set up according to RoHS. | | | | | | | | | | |

2.Process for RoHS requirement：**(only for RoHS inspection)**

  (1) Use the Sn/Ag/Cu soldering surface；the surface of Pb-free solder is rougher than we used before.

  (2) Heat-resistance temp.：

    Reflow：250℃,30 seconds Max.；

    Connector soldering wave or hand soldering：320℃, 10 seconds max.

  (3) Temp. curve of reflow, max. Temp.：235±5℃；

    Recommended customer's soldering temp. of connector：280℃, 3 seconds.

# 13.Recommendable Storage

1. Place the panel or module in the temperature 25°C±5°C and the humidity below 65% RH

2. Do not place the module near organics solvents or corrosive gases.

3. Do not crush, shake, or jolt the module.

**1、** <u>**Panel Specification**</u>：

   1.  Panel Type：         ☐ Pass       ☐ NG ,_____

   2.  View Direction：     ☐ Pass       ☐ NG ,_____

   3.  Numbers of Dots：    ☐ Pass       ☐ NG ,_____

   4.  View Area：         ☐ Pass       ☐ NG ,_____

   5.  Active Area：        ☐ Pass       ☐ NG ,_____

   6.  Operating Temperature： ☐ Pass       ☐ NG ,_____

   7.  Storage Temperature：  ☐ Pass       ☐ NG ,_____

   8.  Others：_____

**2、** <u>**Mechanical Specification**</u>：

   1.  PCB Size：          ☐ Pass       ☐ NG ,_____

   2.  Frame Size：        ☐ Pass       ☐ NG ,_____

   3.  Materal of Frame：   ☐ Pass       ☐ NG ,_____

   4.  Connector Position：  ☐ Pass       ☐ NG ,_____

   5.  Fix Hole Position：   ☐ Pass       ☐ NG ,_____

   6.  Backlight Position：   ☐ Pass       ☐ NG ,_____

   7.  Thickness of PCB：   ☐ Pass       ☐ NG ,_____

   8.  Height of Frame to PCB：☐ Pass       ☐ NG ,_____

   9.  Height of Module：   ☐ Pass       ☐ NG ,_____

   10. Others：          ☐ Pass       ☐ NG ,_____

**3、** <u>**Relative Hole Size**</u>：

   1.  Pitch of Connector：  ☐ Pass       ☐ NG ,_____

   2.  Hole size of Connector：☐ Pass       ☐ NG ,_____

   3.  Mounting Hole size：  ☐ Pass       ☐ NG ,_____

   4.  Mounting Hole Type： ☐ Pass       ☐ NG ,_____

   5.  Others：           ☐ Pass       ☐ NG ,_____

**4、** <u>**Backlight Specification**</u>：

   1. B/L Type：          ☐ Pass       ☐ NG ,_____

   2. B/L Color：         ☐ Pass       ☐ NG ,_____

   3. B/L Driving Voltage (Reference for LED Type)： ☐ Pass     ☐ NG ,_____

   4. B/L Driving Current：  ☐ Pass       ☐ NG ,_____

   5. Brightness of B/L：   ☐ Pass       ☐ NG ,_____

   6. B/L Solder Method：  ☐ Pass       ☐ NG ,_____

   7. Others：           ☐ Pass       ☐ NG ,_____

**5、Electronic Characteristics of Module**：

  1.  Input Voltage：     ☐ Pass     ☐ NG , _____

  2.  Supply Current：     ☐ Pass     ☐ NG , _____

  3.  Driving Voltage for LCD：     ☐ Pass     ☐ NG , _____

  4.  Contrast for LCD：     ☐ Pass     ☐ NG , _____

  5.  B/L Driving Method：     ☐ Pass     ☐ NG , _____

  6.  Negative Voltage Output：     ☐ Pass     ☐ NG , _____

  7.  Interface Function：     ☐ Pass     ☐ NG , _____

  8.  LCD Uniformity：     ☐ Pass     ☐ NG , _____

  9.  ESD test：     ☐ Pass     ☐ NG , _____

 10.  Others：     ☐ Pass     ☐ NG , _____

**6、Summary**：

 

**Sales signature**： _____

**Customer Signature**： _____           **Date**： ___ / ___ / ___